MICRONAS

Hardware
Documentation

Advance Information

HAL® 1504, HAL 1505, HAL 1510

Hall-Effect Switches with Open-Drain Output (3-wire) in TO92UA package

ASIL ready

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Hall-Effect Switches with Open-Drain Output (3-wire) in TO92UA Package

1. Introduction

The Hall-switches HAL 1504, HAL 1505, and HAL 1510 produced in CMOS technology as 3-wire devices with open-drain output transistor include a temperature-compensated Hall plate with active offset compensation, a comparator, and an output stage.

The comparator compares the actual magnetic flux through the Hall plate (Hall voltage) with the fixed reference values (switching points). Accordingly the output transistor is switched on or off.

The active offset compensation leads to constant magnetic characteristics over supply voltage and temperature range. In addition, the magnetic parameters are robust against mechanical stress effects.

The sensors are designed for industrial and automotive applications and operate with supply voltages from 2.7 V to 24 V in the junction temperature range from –40 °C up to 170 °C.

The Hall-switches HAL 1504, HAL 1505, and HAL 1510 are available in a TO92-UA leaded package.

1.1. Features of HAL 1504, HAL 1505, and HAL 1510

– TO92-UA leaded package
– ISO 26262 compliant as ASIL A ready device
– Short-circuit protected open-drain output and thermal shutdown
– Low current consumption of typ. 1.6 mA
– Operates with supply voltages from 2.7 V to 24 V
– Overvoltage protection capability up to 40 V
– Reverse-voltage protected VSUP-pin (−18 V)
– High ESD performance of ±8 kV (HBM)
– Diagnostic features: power-on self test
– Sample frequency of 500 kHz, 2 µs output refresh time
– Operates with static and dynamic magnetic fields up to 12 kHz
– High resistance to mechanical stress by active offset compensation
– Constant switching points over a wide supply voltage and temperature range
– Wide junction temperature range from −40 °C to 170 °C
– Built-in temperature coefficient
– Optimized for applications in extreme automotive and industrial environments
– Qualified according to AEC-Q100 test standard for automotive electronics industry to provide high-quality performance
– Robust EMC performance, corresponding to different standards, such as ISO 7637, ISO 16750, IEC 61967, ISO 11452, and ISO 62132

2. Ordering Information

A Micronas device is available in a variety of delivery forms. They are distinguished by a specific ordering code:

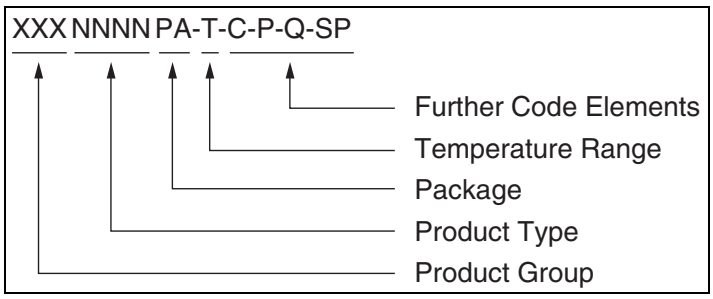

Fig. 2–1: Ordering Code Principle

For detailed information, please refer to the brochure: “Hall Sensors: Ordering Codes, Packaging, Handling”.

2.1. Device-Specific Ordering Codes

HAL 1504, HAL 1505, and HAL 1510 are available in the following package and temperature range.

Table 2–1: Available packages

Package Code (PA)	Package Type
UA	TO92

Table 2–2: Available temperature ranges

Temperature Code (T)	Temperature Range
A	T_J = –40 °C to +170 °C

The relationship between ambient temperature (T_A) and junction temperature (T_J) is explained in **Section 5.2. on page 27**.

For available variants for Configuration (C), Packaging (P), Quantity (Q), and Special Procedure (SP) please contact TDK-Micronas.

Table 2–3: Available ordering codes and corresponding package marking

Available Ordering Codes	Package Marking
HAL1504UA-A-[C-P-Q-SP]	1504A
HAL1505UA-A-[C-P-Q-SP]	1505A
HAL1510UA-A-[C-P-Q-SP]	1510A

3. Functional Description of HAL 1504, HAL 1505, and HAL 1510

The sensors HAL 1504, HAL 1505, and HAL 1510 are monolithic integrated circuits which switch in response to magnetic fields. If a magnetic field with flux lines perpendicular to the sensitive area is applied to the sensor, the biased Hall plate forces a Hall voltage proportional to this field. The Hall voltage is compared with the actual threshold level in the comparator. If the magnetic field exceeds the threshold levels, the output stage is switched to the appropriate state.

The built-in hysteresis eliminates oscillation and provides switching behavior of the output without bouncing.

Offsets caused by mechanical stress are compensated by using the “switching offset compensation technique”.

A diode on the supply line is not required thanks to the built-in reverse voltage protection.

The open drain output is forced to a safe, High-Z (high-impedance) state, in any of the following fault conditions: overtemperature, undervoltage and functional safety related diagnoses (see Section 3.1.). In addition, the output current is limited (short-circuit protection).

The devices are able to withstand a maximum supply voltage of 24 V over lifetime and features overvoltage capability (40 V load dump).

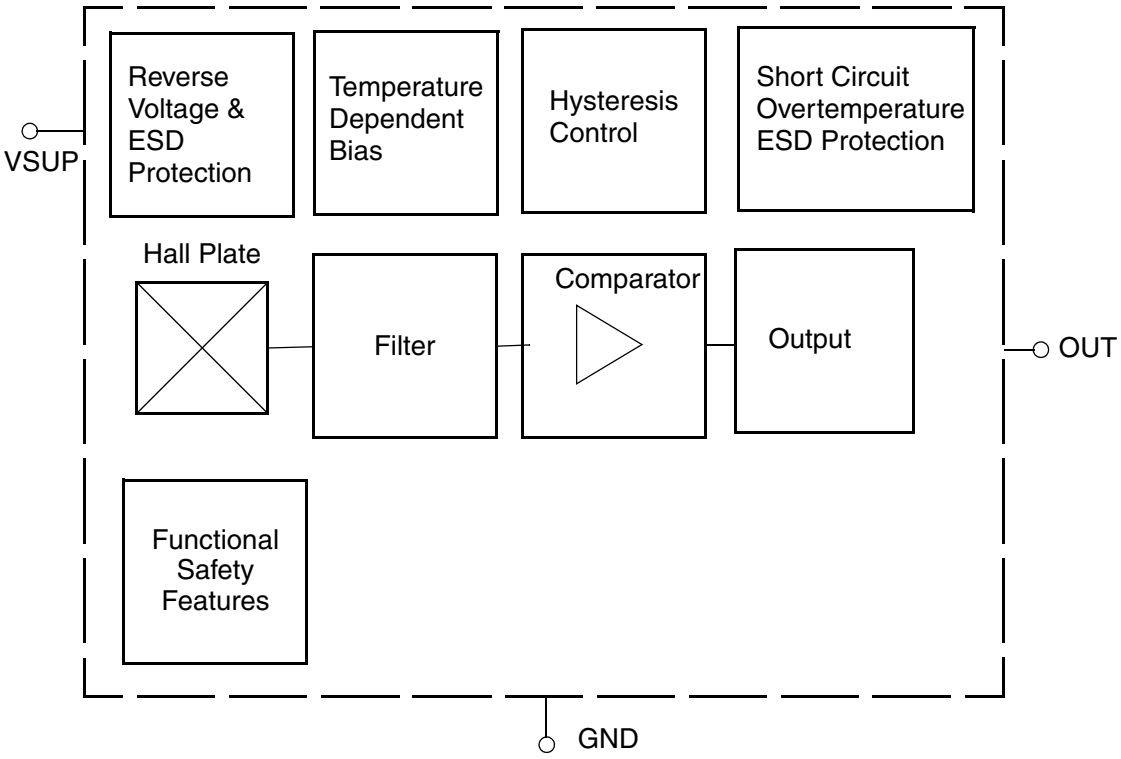

Fig. 3–1: Block diagram HAL 1504, HAL 1505, and HAL 1510

3.1. Functional Safety According to ISO 26262

HAL 1504, HAL 1505, and HAL 1510 are ISO 26262 compliant as ASIL A ready devices.

Magnetic and switching performance is defined as a hardware safety requirement.

The safe state is defined as High-Z output.

3.1.1. Diagnostic Features

Internal states are monitored and in an error condition flagged with a High-Z at the output:

– Internal voltage regulator: under and over voltage detection

– Monitoring of internal bias and current levels

– Monitoring of the internal reference voltage

– Monitoring of the Hall plate voltage

Note For further documentation regarding functional safety please contact TDK-Micronas.

3.2. Power-On Self-Test

The power-on self-test allows the customer to execute a functional check of the device, as well as to detect wire breaks as long as the host controls the power supply of the device.

The self-test can be enabled only once after power-on.

In order to start the test, the host has to power off the sensor and to pull down its output pin. Afterwards, the host needs to power on the sensor again (sensor in High-Z mode, after waking up) and then to release its output pin. This order of events is the criteria for the sensor to start the power-on self-test.

After releasing the output pin, the sensors simulate a magnetic field for a pre-defined period of time (see first observation window in Fig. 3–2), driving the sensor's output to low level, detected by the host.

Subsequently, the sensors simulate an opposite magnetic field during the second observation window (see Fig. 3–2), driving the sensor's output to high level, also detected by the host. The described self-test behavior is not impacted by external magnetic fields up to about 300 mT.

After self-test completion, the sensors always return to normal operation regardless of the test result.

By positioning the pull-up resistor close to the control unit, wire breaks at all pins VSUP, OUT, and GND can be detected.

It is also possible to enable the power-on self-test in application systems, consisting of several HAL15xy 3-wire sensors, as long, as the output pins are not connected to each other.

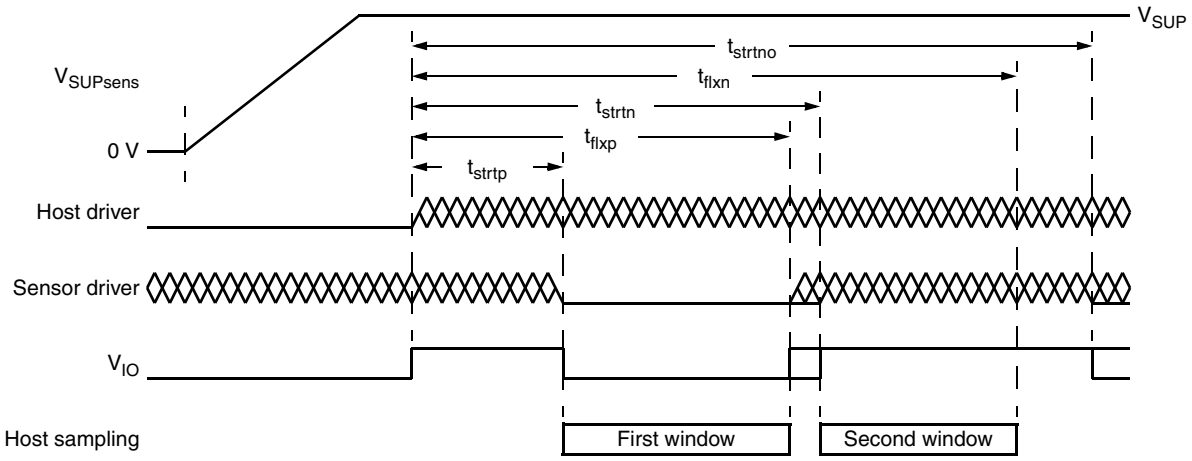

Fig. 3–2: Self-test timing diagram

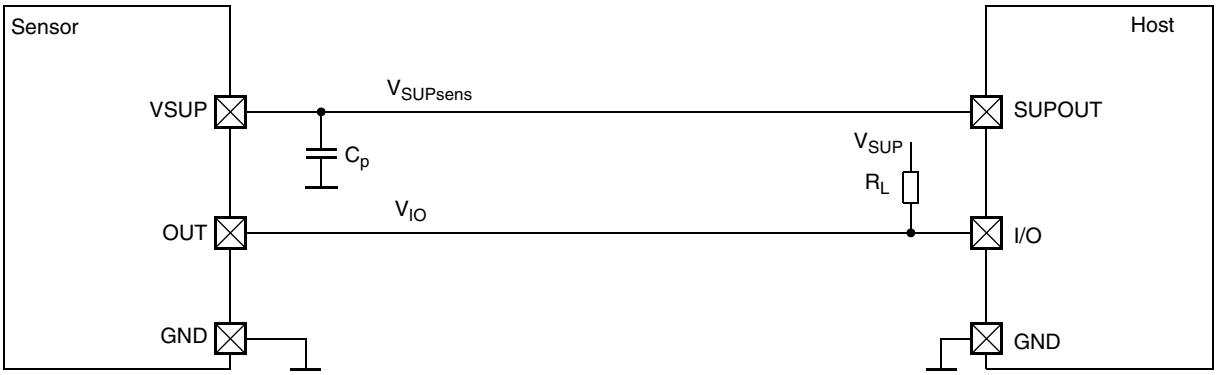

Fig. 3–3: External circuit diagram with switchable supply

4. Specifications

4.1. Outline Dimensions

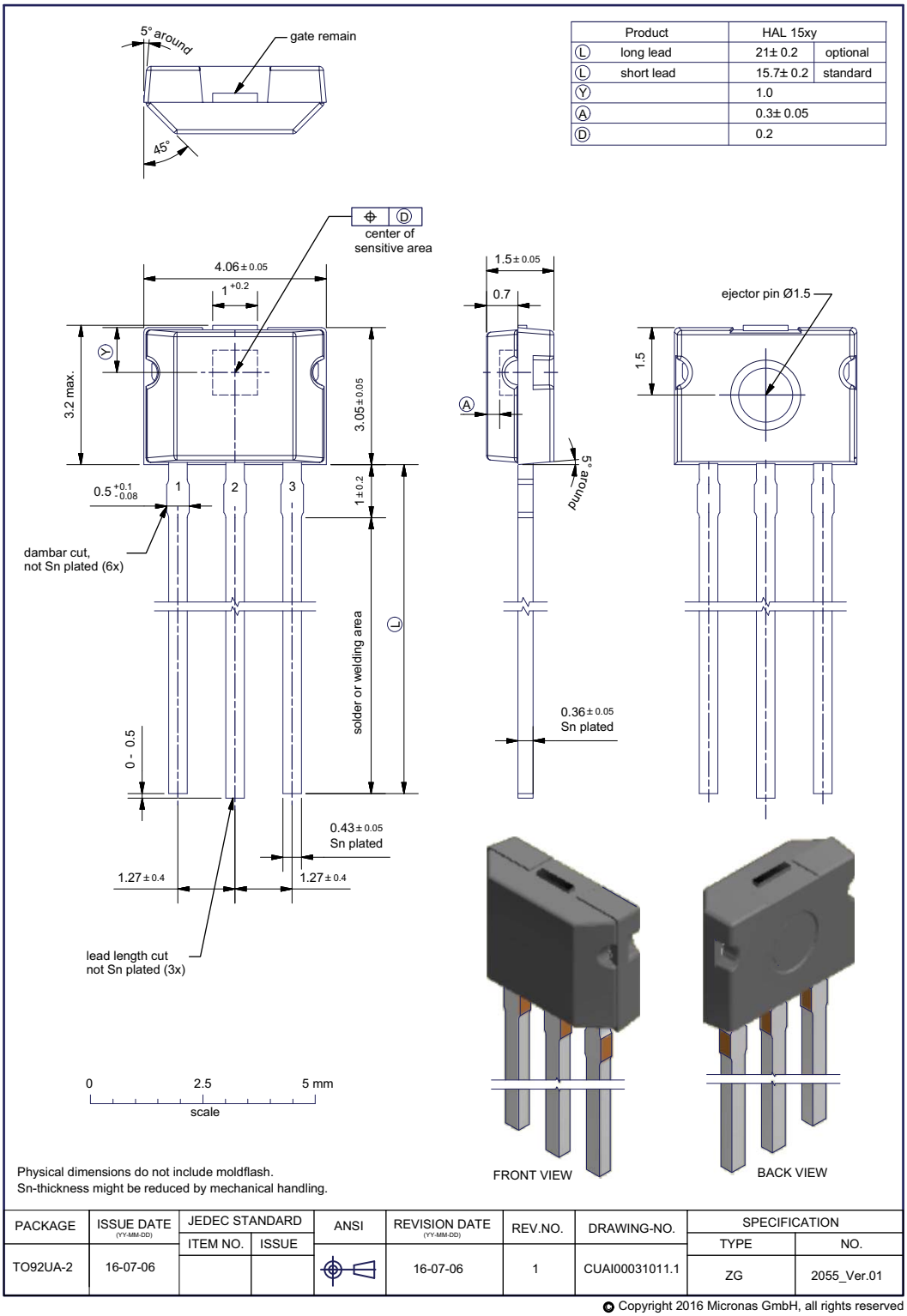

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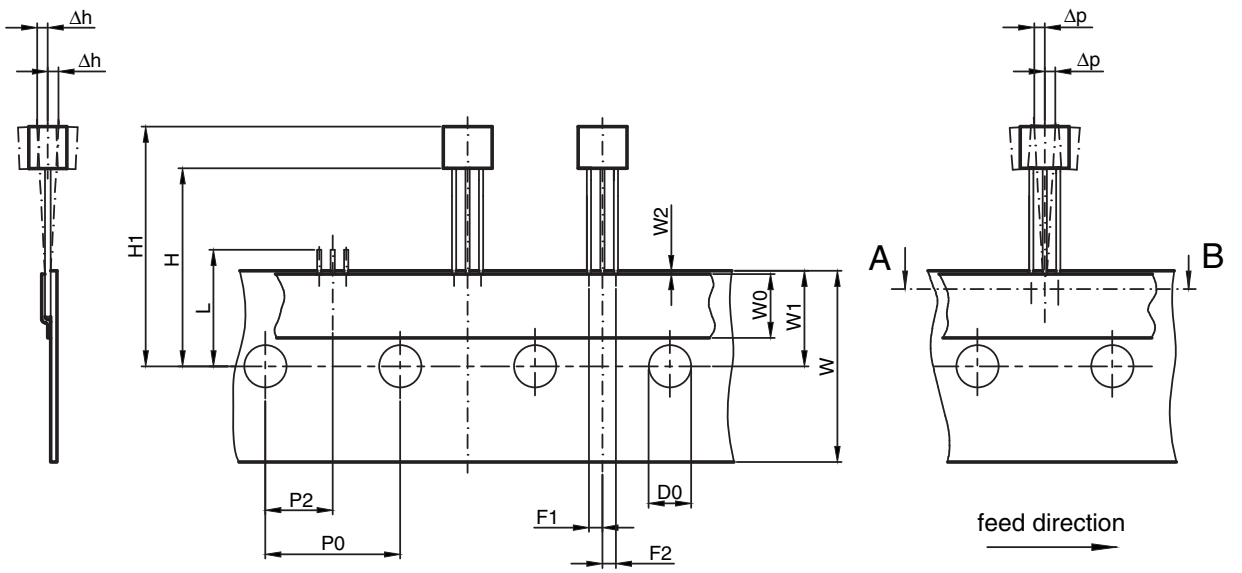
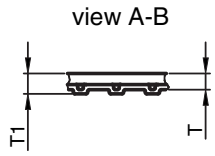
Fig. 4–1:
TO92UA-2: Plastic Transistor Standard UA package, 3 leads, non-spread
Weight approximately 0.106 g





all dimensions in mm
other dimensions see drawing of bulk
max. allowed tolerance over 20 hole spacings ±1.0

	H	H1	
		TO92UA	TO92UT
Short leads	18 - 20	21 - 23.1	22 - 24.1
Long leads	24 - 26	27 - 29.1	28 - 30.1

UNIT	D0	F1	F2	Δh	L	P0	P2	Δp	T	T1	W	W0	W1	W2
mm	4.0	1.47 1.07	1.47 1.07	±1.0	11.0 max	13.2 12.2	7.05 5.65	±1.0	0.5	0.9	18.0	6.0	9.0	0.3

STANDARD		ANSI	ISSUE DATE YY-MM-DD	DRAWING-NO.	ZG-NO.
ISSUE	ITEM NO.				
-	IEC 60286-2		16-07-18	06631.0001.4	ZG001031_Ver.05

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Fig. 4–2:
TO92UA/UT: Dimensions ammopack inline, not spread

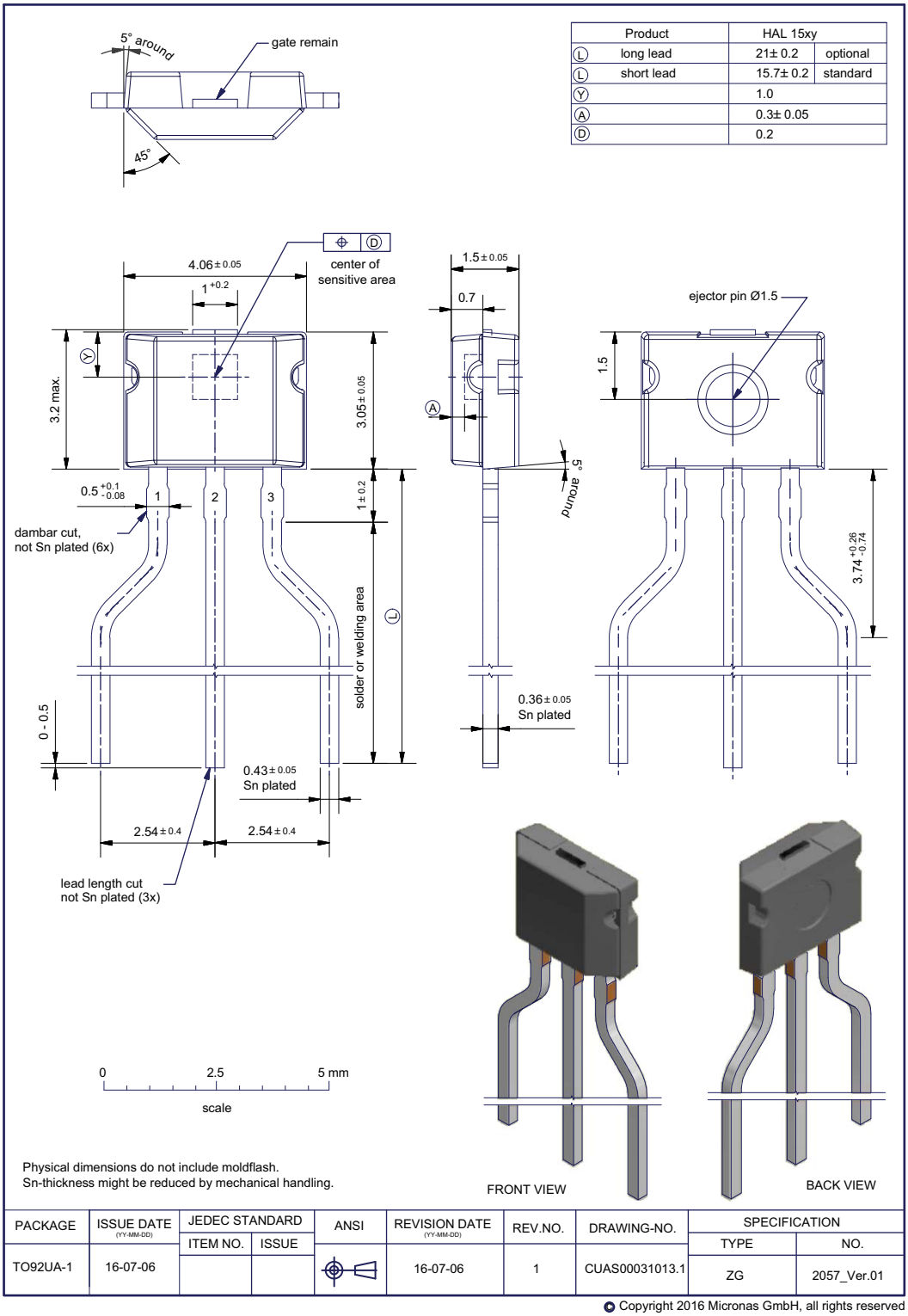

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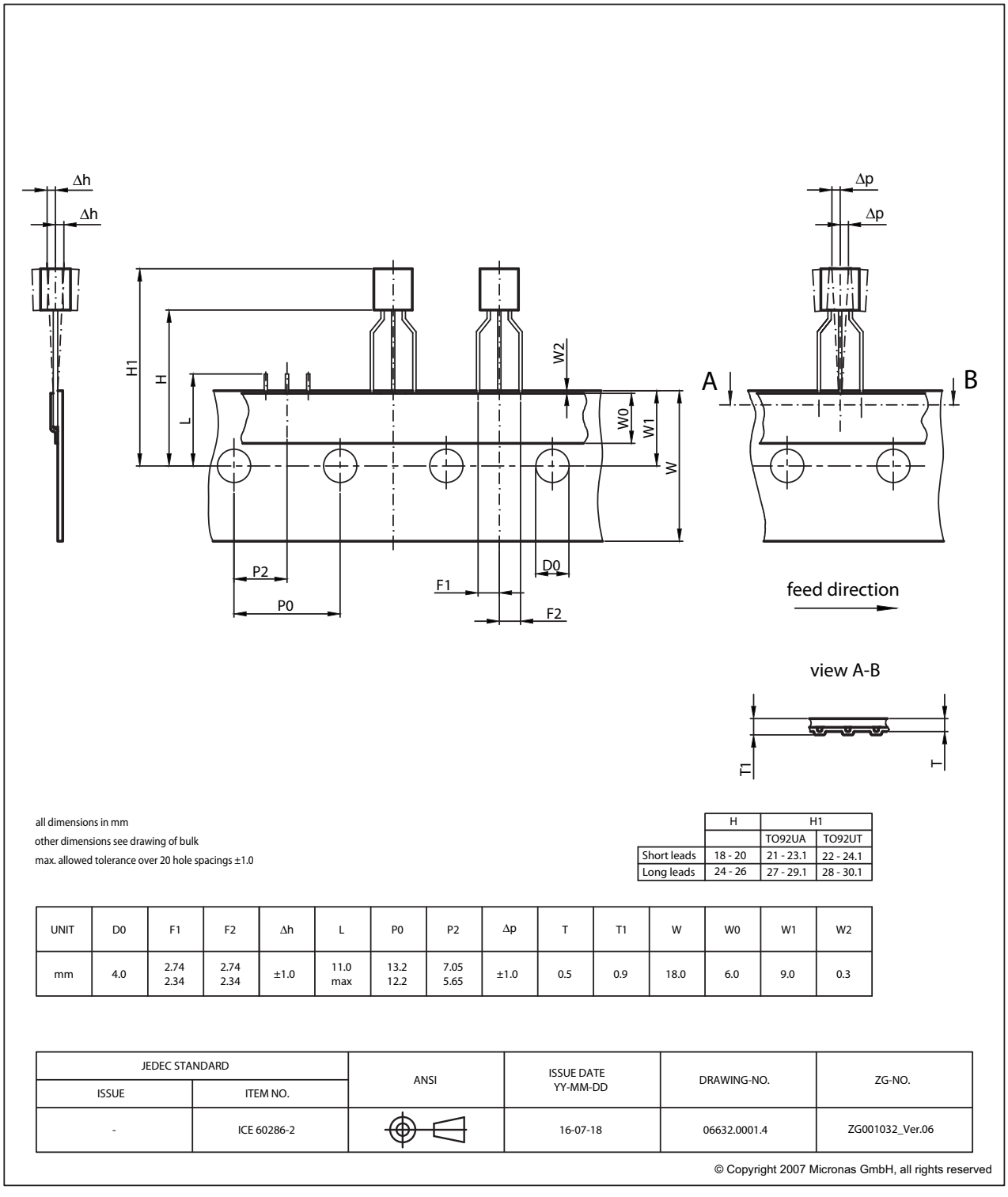
Fig. 4–3:
TO92UA-1: Plastic Transistor Standard UA package, 3 leads, spread
Weight approximately 0.106 g



all dimensions in mm
other dimensions see drawing of bulk
max. allowed tolerance over 20 hole spacings ±1.0

	H	H1	
		TO92UA	TO92UT
Short leads	18 - 20	21 - 23.1	22 - 24.1
Long leads	24 - 26	27 - 29.1	28 - 30.1

UNIT	D0	F1	F2	Δh	L	P0	P2	Δp	T	T1	W	W0	W1	W2
mm	4.0	2.74 2.34	2.74 2.34	±1.0	11.0 max	13.2 12.2	7.05 5.65	±1.0	0.5	0.9	18.0	6.0	9.0	0.3

JEDEC STANDARD		ANSI	ISSUE DATE YY-MM-DD	DRAWING-NO.	ZG-NO.
ISSUE	ITEM NO.				
-	ICE 60286-2		16-07-18	06632.0001.4	ZG001032_Ver.06

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Fig. 4–4:
TO92UA/UT: Dimensions ammopack inline, spread

4.2. Soldering, Welding and Assembly

Information related to solderability, welding, assembly, and second-level packaging is included in the document “Guidelines for the Assembly of Micronas Packages”.
It is available on the TDK-Micronas website (http://www.micronas.com/en/service-center/downloads) or on the service portal (http://service.micronas.com).

4.3. Pin Connections (from Top Side, example HAL 1505) and Short Descriptions

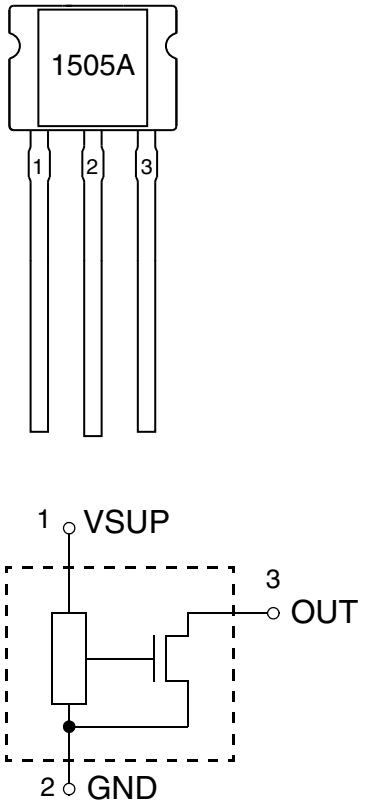


Fig. 4–5: Pin configuration

Table 4–1: Pin assignment.

Pin number	Name	Function
1	VSUP	Supply voltage
3	OUT	Output
2	GND	Ground

4.3.1. Dimension of Sensitive Area

Parameter	Min.	Typ.	Max.	Unit
Dimension of sensitive area	–	100 x 100	–	μm^2

4.4. Absolute Maximum Ratings

Stresses beyond those listed in the “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only. Functional operation of the device at these conditions is not implied. Exposure to absolute maximum rating conditions for extended periods will affect device reliability.

This device contains circuitry to protect the inputs and outputs against damage due to high static voltages or electric fields; however, it is advised that normal precautions be taken to avoid application of any voltage higher than absolute maximum-rated voltages to this circuit.

All voltages listed are referenced to ground (GND).

Symbol	Parameter	Pin No	Min.	Max.	Unit	Conditions
T_J	Junction temperature range A	–	–40	190	°C	t < 96 h[1)]
V_{SUP}	Supply voltage	1	–18	28	V	t < 96 h[1)]
			–	32	V	t < 5 min[1)]
			–	40	V	t < 10 x 400 ms “Load-Dump”[1)] with series resistor R_V > 100 Ω
V_{OUT}	Output voltage	2	–0.5	28	V	t < 96 h[1)]
I_O	Output current	2	–	65	mA	
I_{OR}	Reverse output current	2	–50		mA	

[1)] No cumulative stress

4.5. ESD and Latch-up

The output pin has to be in High-Z for ESD measurements.

Table 4–2: ESD and latch-up

Symbol	Parameter	Min.	Max.	Unit
I_{latch}	Maximum latch-up free current at any pin (measurement according to AEC Q100-004), class 1	–100	100	mA
V_{HBM}	Human body model (according to AEC Q100-002)	–8	8	kV
V_{CDM}	Charged device model (according to AEC Q100-011)	–1	1	kV
V_{SYSTEM_LEVEL}	Unpowered Gun Test (150 pF / 330 Ω or 330 pF / 2 kΩ) according to ISO 10605-2008[1)]	–15	15	kV

[1)]only valid with ESD System Level Application Circuit (see Fig. 5–2 on page 28)

4.6. Storage and Shelf Life

Information related to storage conditions of Micronas sensors is included in the document “Guidelines for the Assembly of Micronas Packages”. It gives recommendations linked to moisture sensitivity level and long-term storage.
It is available on the TDK-Micronas website (http://www.micronas.com/en/service-center/downloads) or on the service portal (http://service.micronas.com).

4.7. Recommended Operating Conditions

Functional operation of the device beyond those indicated in the “Recommended Operating Conditions” of this specification is not implied, may result in unpredictable behavior of the device, and may reduce reliability and lifetime.

All voltages listed are referenced to ground (GND).

Symbol	Parameter	Pin No	Min.	Typ.	Max.	Unit	Conditions
V_{SUP}	Supply voltage	1	2.7		24	V	
$\Delta V_{SUP} / \Delta t$	Power-down slope[1)]	1	–	–	0.1	V/µs	V_{SUP} below 2.7 V
T_J	Junction temperature range A [2)]	–	–40		170 150 125	°C	t < 1000 h[3)] t < 2500 h[3)] t < 8000 h[3)]
V_{OUT}	Output voltage	2			24	V	
I_{OUT}	Output current	2			25	mA	

[1)] This parameter is relevant for ISO26262 applications: In order to ensure the defined output state (High-Z) during power-up in the range below the recommended supply voltage, the preceding power down slope is required to be slower than the maximum $\Delta V_{SUP} / \Delta t$ value.

[2)] Depends on the temperature profile of the application. Please contact TDK-Micronas for life time calculations.

[3)] No cumulative stress

4.8. Characteristics

At T_J = –40 °C to +170 °C, V_{SUP} = 2.7 V to 24 V,
at Recommended Operating Conditions if not otherwise specified in the column “Conditions”.
Typical Characteristics for T_J = 25 °C and V_{SUP} = 12 V

Symbol	Parameter	Pin No.	Min.	Typ.	Max.	Unit	Conditions
Supply							
I_{SUP}	Supply current	1	1.1	1.6	2.4	mA	
I_{SUPR}	Reverse current	1	–1		–	mA	for V_{SUP} = -18 V
Port Output							
V_{ol}	Port low output voltage	2		0.13	0.4	V	I_O = 20 mA
					0.5	V	I_O = 25 mA
I_{oleak}	Output leakage current			0.1	10	µA	
t_f	Output fall time[1]	–			1	µs	V_{SUP} = 12 V; R_L = 820 Ω; C_L = 20 pF
t_r	Output rise time[1]	–			1	µs	
B_{noise}	Effective noise of magnetic switching points (RMS)[2]	–		72		µT	For square wave signal with 12 kHz
t_j	Output jitter (RMS)[1]	–		±0.58	±0.72	µs	For square wave signal with 1 kHz. Jitter is evenly distributed between –1 µs and +1 µs
t_d	Delay time[2] [3]	–		16	21	µs	
t_{samp}	Output refresh period[2]	–	1.6	2.2	3.0	µs	
t_{en}	Enable time of output after settling of V_{SUP} [4]	–		50	60	µs	V_{SUP} = 12 V B > B_{on} + 2 mT or B < B_{off} - 2 mT

[1] Not tested, characterized only
[2] Guaranteed by design
[3] Systematic delay between magnetic threshold reached and output switching
[4] If power-on self-test is executed, t_{en} will be extended by power-on self-test period (see Section 3.2. on page 8)

Symbol	Parameter	Pin No.	Min.	Typ.	Max.	Unit	Conditions
Power-on Self-Test							
t_{dsamp}	Double sample period[2)]	–	3.2	4.4	6.0	µs	
t_{strtp}	Start of first sampling window[2)]	–	–	–	4	t_{dsamp}	
t_{flxp}	End of first sampling window[2)]	–	9	–	–	t_{dsamp}	
t_{strtn}	Start of second sampling window[2)]	–	–	–	10	t_{dsamp}	
t_{flxn}	End of second sampling window[2)]	–	31	–	–	t_{dsamp}	
t_{strtno}	Start of first normal operation value[2)]	–	–	36.5	37	t_{dsamp}	
Package							
R_{thja}	Thermal Resistance junction to air	–	–	–	234	K/W	Measured with a 1s0p board
		–	–	–	180	K/W	Measured with a 1s1p board
		–	–	–	159	K/W	Measured with a 2s2p board
R_{thjc}	Thermal Resistance junction to case	–	–	–	58	K/W	Measured with a 1s0p board
		–	–	–	55	K/W	Measured with a 1s1p board
		–	–	–	53	K/W	Measured with a 2s2p board

[2)] Guaranteed by design

4.9. HAL 1504 Magnetic Characteristics

The HAL 1504 Hall latch provides high sensitivity (see Fig. 4–6 on page 19).

The output turns low with the magnetic south pole on the top side of the package and turns high with the magnetic north pole on the top side. The output does not change if the magnetic field is removed. For changing the output state, the opposite magnetic field polarity must be applied.

For correct functioning in the application, the sensor requires both magnetic polarities (north and south) on the top side of the package.

Magnetic Features:

– switching type: latching

– high sensitivity

– typical B_{ON}: 7.6 mT at room temperature

– typical B_{OFF}: −7.6 mT at room temperature

– operates with static magnetic fields and dynamic magnetic fields up to 12 kHz

– typical temperature coefficient of magnetic switching points is −1200 ppm/K at room temperature

Applications

The HAL 1504 is the optimal sensor for all applications with alternating magnetic signals such as:

– applications with large air gap or weak magnets

– revolutions per minute (RPM) or other counting measurement, e.g. window lifter and sunroof

– commutation of brushless DC motors

– position detection, such as for gear-shift lever and electric parking brake

– magnetic encoders

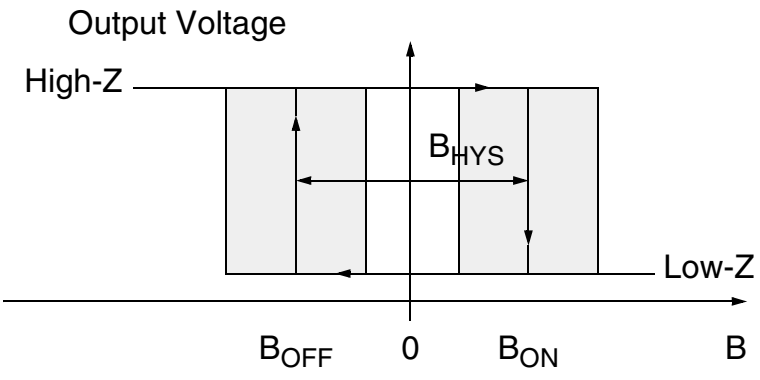


Fig. 4–6: Definition of magnetic switching points for the HAL 1504

Magnetic Characteristics at T_J = –40 °C to +170 °C, V_{DD} = 2.7 V to 24 V, Typical Characteristics for V_{DD} = 12 V

Magnetic flux density values of switching points:
Positive flux density values refer to the magnetic south pole at the top side of the package.

Parameter	On point B_{ON}			Off point B_{OFF}			Hysteresis B_{HYS}			Unit
T_J	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
–40 °C	6.4	8.4	10.8	–10.8	–8.6	–6.4	–	17.0	–	mT
25 °C	6	7.6	10	–10	–7.6	–6	–	15.2	–	mT
170 °C	4	6.4	8.9	–8.9	–6.4	–4	–	12.8	–	mT

The hysteresis is the difference between the switching points $B_{HYS} = | B_{ON} - B_{OFF} |$

Note Regarding switching points, temperature coefficients, and B-field switching frequency, customized derivatives via mask option are possible. For more information contact TDK-Micronas.

4.10. HAL 1505 Magnetic Characteristics

The HAL 1505 Hall-latch provides medium sensitivity (see Fig. 4–7 on page 21).

The output turns low with the magnetic south pole on the top side of the package and turns high with the magnetic north pole on the top side. The output does not change if the magnetic field is removed. For changing the output state, the opposite magnetic field polarity must be applied.

For correct functioning in the application, the sensor requires both magnetic polarities (north and south) on the top side of the package.

Magnetic Features:

– switching type: latching
– medium sensitivity
– typical B_{ON}: 13.5 mT at room temperature
– typical B_{OFF}: −13.5 mT at room temperature
– operates with static magnetic fields and dynamic magnetic fields up to 12 kHz
– typical temperature coefficient of magnetic switching points is −1200 ppm/K at room temperature

Applications

The HAL 1505 is the optimal sensor for all applications with alternating magnetic signals such as:

– applications with large air gap or weak magnets
– revolutions per minute (RPM) or other counting measurement, e.g. window lifter and sunroof
– commutation of brushless DC motors
– position detection, such as for adaptive front lighting and electric parking brake
– magnetic encoders

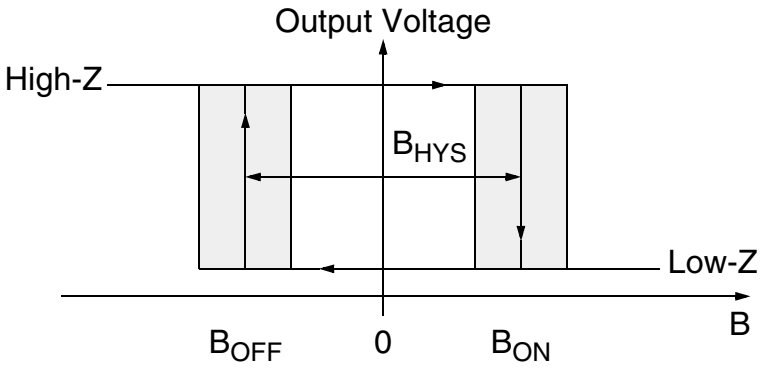


Fig. 4–7: Definition of magnetic switching points for the HAL 1505

Magnetic Characteristics at T_J = –40 °C to +170 °C, V_{DD} = 2.7 V to 24 V, Typical Characteristics for V_{DD} = 12 V

Magnetic flux density values of switching points:
Positive flux density values refer to the magnetic south pole at the top side of the package.

Parameter	On point B_{ON}			Off point B_{OFF}			Hysteresis B_{HYS}			Unit
T_J	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
–40 °C	11.8	15	18.3	–18.3	–15	–11.8	–	30	–	mT
25 °C	11	13.5	17	–17	–13.5	–11	–	27	–	mT
170 °C	9.4	11.7	16.1	–16.1	–11.7	–9.4	–	23.4	–	mT

The hysteresis is the difference between the switching points $B_{HYS} = | B_{ON} - B_{OFF} |$

Note Regarding switching points, temperature coefficients, and B-field switching frequency, customized derivatives via mask option are possible. For more information contact TDK-Micronas.

4.11.HAL 1510 Magnetic Characteristics

The HAL 1510 unipolar Hall switch provides medium sensitivity (see Fig. 4–8 on page 23).

The output turns low with the magnetic south pole on the top side of the package and turns high if the magnetic field is removed. The sensor does not respond to the magnetic north pole on the top side of the package.

For correct functioning in the application, the sensor requires only the magnetic south pole on the top side of the package.

Magnetic Features:

– switching type: unipolar
– medium sensitivity
– typical B_{ON}: 12 mT at room temperature
– typical B_{OFF}: 7 mT at room temperature
– operates with static magnetic fields and dynamic magnetic fields up to 12 kHz
– typical temperature coefficient of magnetic switching points is –1200 ppm/K at room temperature

Applications

The HAL 1510 is the optimal sensor for all applications with one magnetic polarity and, such as:

– clutch position detection
– electric parking brake
– brake light switch
– brake pedal position detection
– steering wheel lock
– door handle

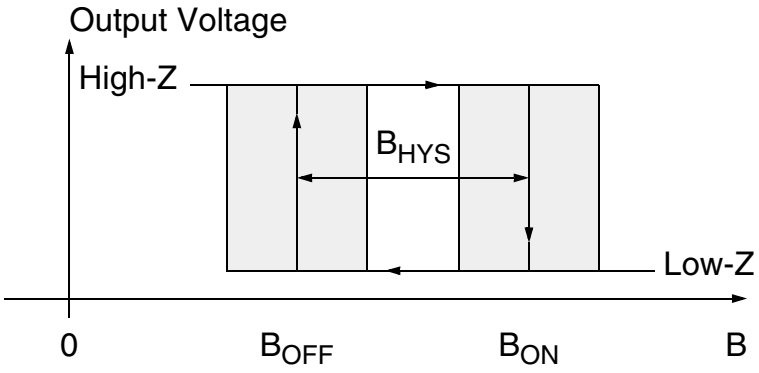


Fig. 4–8: Definition of magnetic switching points for the HAL 1510

Magnetic Characteristics at T_J = –40 °C to +170 °C, V_{DD} = 2.7 V to 24 V, Typical Characteristics for V_{DD} = 12 V

Magnetic flux density values of switching points:
Positive flux density values refer to the magnetic south pole at the top side of the package.

Parameter	On point B_{ON}			Off point B_{OFF}			Hysteresis B_{HYS}			Unit
T_J	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
–40 °C	10.3	13	15.7	5.3	7.5	9.6	–	5.5	–	mT
25 °C	9.5	12	14.5	5	7	9	–	5	–	mT
170 °C	8.5	10.2	13.7	4.2	5.9	8.5	–	4.3	–	mT

The hysteresis is the difference between the switching points $B_{HYS} = | B_{ON} - B_{OFF} |$

Note Regarding switching points, temperature coefficients, and B-field switching frequency, customized derivatives via mask option are possible. For more information contact TDK-Micronas.

5. Application Notes

5.1. Application Circuits

For applications with disturbances on the supply line or radiated disturbances, a series resistor R_V and two capacitors C_P and C_L all placed close to the sensor are recommended (see Fig. 5–1).
For example: R_V =100 Ω, C_P = 10 nF, and C_L = 4.7 nF.

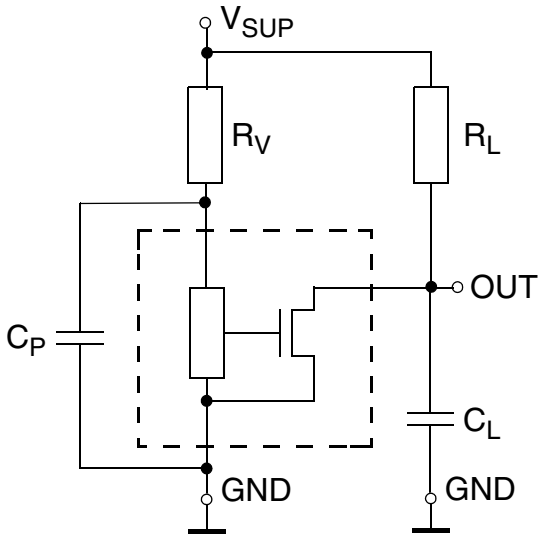
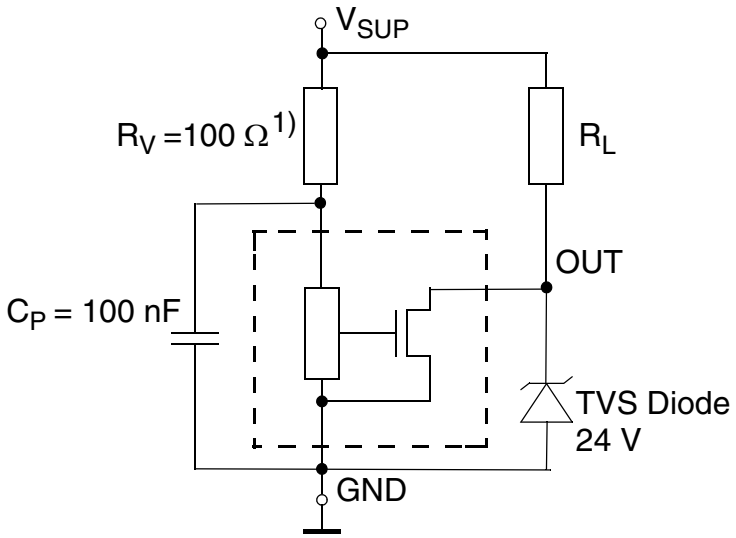


Fig. 5–1: Example for a recommended application circuit

R_L is the open-drain pull-up resistor and has to be placed close to the input of the host controller to enable wire-break detection.

5.1.1. ESD System Level Application Circuit (ISO10605-2008)

For an ESD system level application circuit according to ISO10605-2008 a 100 nF capacitor at VSUP and an additional TVS diode at OUT are necessary.



1) required for 40 V load dump capability

Fig. 5–2: Application circuit with external resistor

5.2. Ambient Temperature

Due to the internal power dissipation, the temperature on the silicon chip (junction temperature T_J) is higher than the temperature outside the package (ambient temperature T_A).

$$T_J = T_A + \Delta T$$

Under static conditions and continuous operation, the following equation applies:

$$\Delta T = (I_{SUP} \times V_{SUP} \times R_{thja}) + (I_{OUT} \times V_{OUT} \times R_{thja})$$

For all sensors, the junction temperature range T_J is specified. The maximum ambient temperature T_{Amax} can be calculated as:

$$T_{Amax} = T_{Jmax} - \Delta T$$

For typical values, use the typical parameters. For worst case calculation, use the max. parameters for I_{SUP} , I_{OUT} , and R_{thja}, and the max. value for V_{OUT} and V_{SUP} from the application.

5.3. Start-Up Behavior

The sensors have an initialization time (enable time t_{en}) after applying the supply voltage. The parameter t_{en} is specified in the Electrical Characteristics (see page 17).

During the initialization time, the output state is defined as High-Z.

After t_{en}, the output will be Low-Z if the applied magnetic field B is above B_{ON}. The output will be High-Z if B is below B_{OFF}.

Note For magnetic fields between B_{OFF} and B_{ON} after applying V_{SUP} , the output state of the device will be High-Z.

For further information see Application Notes for HAL 15xy.

5.4. EMC and ESD

For applications with disturbances on the supply line or radiated disturbances, a series resistor and a capacitor are recommended. The series resistor and the capacitor should be placed as close as possible to the HAL sensor.

Special application arrangements were evaluated to pass EMC tests according to different standards, such as ISO 7637, ISO 16750, IEC 61967, ISO 11452 and ISO 62132.

6. Document History

1. Advance Information: “HAL 1504, HAL 1505, HAL 1510, Hall-Effect Switches with Open-Drain Output (3-wire) in TO92UA Package”, Dec. 13, 2016; AI000198_001EN. First release of the Advance Information.

TDK-Micronas GmbH
Hans-Bunte-Strasse 19 · D-79108 Freiburg · P.O. Box 840 · D-79008 Freiburg, Germany
Tel. +49-761-517-0 · Fax +49-761-517-2174 · E-mail: docservice@micronas.com · Internet: www.micronas.com