EXAMINED BY： Jacky-Kuo	EMERGING DISPLAY TECHNOLOGIES CORPORATION	FILE NO. CAS-P00934
		ISSUE ：SEP.12, 2024
APPROVED BY: Justin Horng		TOTAL PAGE：26
		VERSION：P

CUSTOMER ACCEPTANCE SPECIFICATIONS

MODEL NO.：

ETML121013DADHA

(RoHS)

FOR MESSRS：

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CUSTOMER'S APPROVAL

DATE：

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RECORDS OF REVISION

DOC. FIRST ISSUE

SEP.12, 2024

DATE	REVISED PAGE NO.	SUMMARY

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1. GENERAL SPECIFICATIONS

1.1 DATA SHEET FOR CONTROLLER/DRIVER
PLEASE REFER TO：

JD9168S

1.2 DATA SHEET FOR CAPACITIVE TOUCH PANEL CONTROLLER/DRIVER
PLEASE REFER TO：

ILITEK ILI2521

1.3 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED
DIPHENYL ETHERS (PBDE), BIS(2-ETHYLHEXYL) PHTHALATE (DEHP), BUTYL BENZYL PHTHALATE (BBP), DIBUTYL PHTHALATE (DBP), DIISOBUTYL PHTHALATE (DIBP).

2. MECHANICAL SPECIFICATIONS

(1)	DISPLAY SIZE	12.1 inch
(2)	NUMBER OF DOTS	1024(RGB)W * 768H DOTS
(3)	MODULE SIZE	260.5W * 204H * 10D mm
(4)	VIEWING AREA	247.76W * 186.32H mm
(5)	ACTIVE AREA	245.76W * 184.32H mm
(6)	DOT SIZE	0.08W * 0.24H mm
(7)	PIXEL SIZE	0.24W * 0.24H mm
(8)	LCD TYPE	TFT, IPS, TRANSMISSIVE, NORMALLY BLACK
(9)	COLOR	16.7M
(10)	VIEWING DIRECTION	SUPER WIDE VIEW
(11)	BACKLIGHT	LED , COLOR : WHITE
(12)	INTERFACE MODE	LVDS (8BIT) DE-MODE
(13)	WEIGHT	TBD

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2.2 CAPACITIVE TOUCH PANEL MECHANICAL SPECIFICATIONS

(1) TOUCH PANEL SIZE	----------	12.1 inch
(2) OUTER DIMENSION	----------	258.5W * 201H * 2D mm (NOT INCLUDED FPC)
(3) ACTIVE AREA	----------	247.76W * 186.32H mm
(4) INPUT TYPE	----------	MULTI TOUCH
(5) NUMBER OF TOUCH SENSOR	----------	50 * 37 SENSORS
(6) RESOLUTION	----------	16384*9600
(7) INTERFACE MODE	----------	USB

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3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.3	3.6	V	
STATIC ELECTRICITY	—	—	—	V	NOTE (1)

NOTE (1)：LCM SHOULD BE GROUND DURING LCM HANDLING.

NOTE (2)：THE ABSOLUTE MAXIMUM RATING VALUES OF THIS PRODUCT ARE NOT ALLOWED TO BE EXCEEDED AT ANY TIMES. SHOULD A MODULE BE USED WITH ANY OF THE ABSOLUTE MAXIMUM RATINGS EXCEEDED, THE CHARACTERISTICS OF THE MODULE MAY NOT BE RECOVERED, OR IN AN EXTREME CASE, THE MODULE MAY BE PERMANENTLY DESTROYED.

3.2 CAPACITIVE TOUCH PANEL ELECTRICAL ABSOLUTE MAXIMUM RATINGS

Ta=25°C

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD1-VSS1	-0.3	6	V	

3.3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		REMARK
	MIN.	MAX.	MIN.	MAX.	
AMBIENT TEMPERATURE	-20 °C	70 °C	-30 °C	80 °C	NOTE (1), (2), (3)
HUMIDITY	NOTE (3)		NOTE (3)		WITHOUT CONDENSATION
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE		

NOTE (1)：THE ABSOLUTE MAXIMUM RATINGS OF THIS PRODUCT SHOULD NOT BE EXCEEDED AT ANY TIME. IF THESE RATINGS ARE EXCEEDED, THE PRODUCT'S PERFORMANCE IS NOT GUARANTEED AND THE PRODUCT MAY EXPERIENCE PERMANENT DAMAGE.

NOTE (2)：BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3)：Ta ≤ 60°C：90%RH MAX.

Ta > 60°C：ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C.

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4. ELECTRICAL CHARACTERISTICS

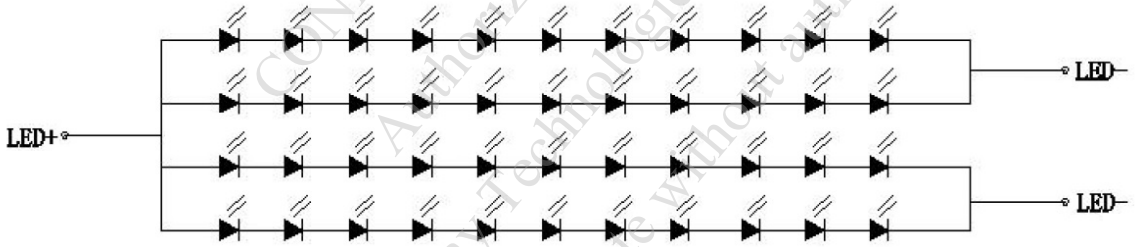
4.1 TFT LCD ELECTRICAL CHARACTERISTICS

Ta = 25 °C

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE		VDD-VSS	—	3.15	3.3	3.45	V	
LOGIC HIGH INPUT VOLTAGE		VIH	—	0.7VDD	—	VDD	V	
LOGIC LOW INPUT VOLTAGE		VIL	—	VSS	—	0.3VDD	V	
POWER SUPPLY CURRENT		IDD	VDD-VSS =3.3V	—	(156.5)	(234. 75)	mA	NOTE (1)
POWER SUPPLY VOLTAGE FOR LED DRIVER		VBL+-VSS	—	(11.5)	(12.0)	(12.5)	V	NOTE (2)
PWM	PWM CONTROL LEVEL	BL ON	—	2.5	3.3	5	V	
		BL OFF		0	—	0.5		
	PWM DIMMING FREQUENCY	—	—	20	—	1000	KHz	
	PWM DUTY	—	—	5	—	100	%	
EN	EN CONTROL LEVEL	BL ON	—	2.5	3.3	5.0	V	
		BL OFF	—	0	—	0.5	V	
POWER SUPPLY CURRENT FOR LED DRIVER		IBL	VBL+-VSS=12.0V LED B/L=ON PWM=100%	—	(825)	(990)	mA	
LED LIFE TIME		—	IF=70 mA (PER LED)	(30000)	—	—	HRS	NOTE (3) NOTE (4)

NOTE (1) : THE DISPLAY PATTERN IS ALL “WHITE”.

NOTE (2) : INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT.



NOTE (3) : CONDITIONS; Ta=25 °C, CONTINUOUS LIGHTING

NOTE (4) : DEFINITIONS OF LIFE TIME :
LCM LUMINANCE BECOMES HALF OF THE INITIAL VALUE.

4.2 CAPACITIVE TOUCH PANEL ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
POWER SUPPLY VOLTAGE	VDD1-VSS1	—	4.75	5.0	5.25	V
POWER SUPPLY CURRENT	IDD1	VDD1-VSS1 =5.0V	—	(90)	(120)	mA

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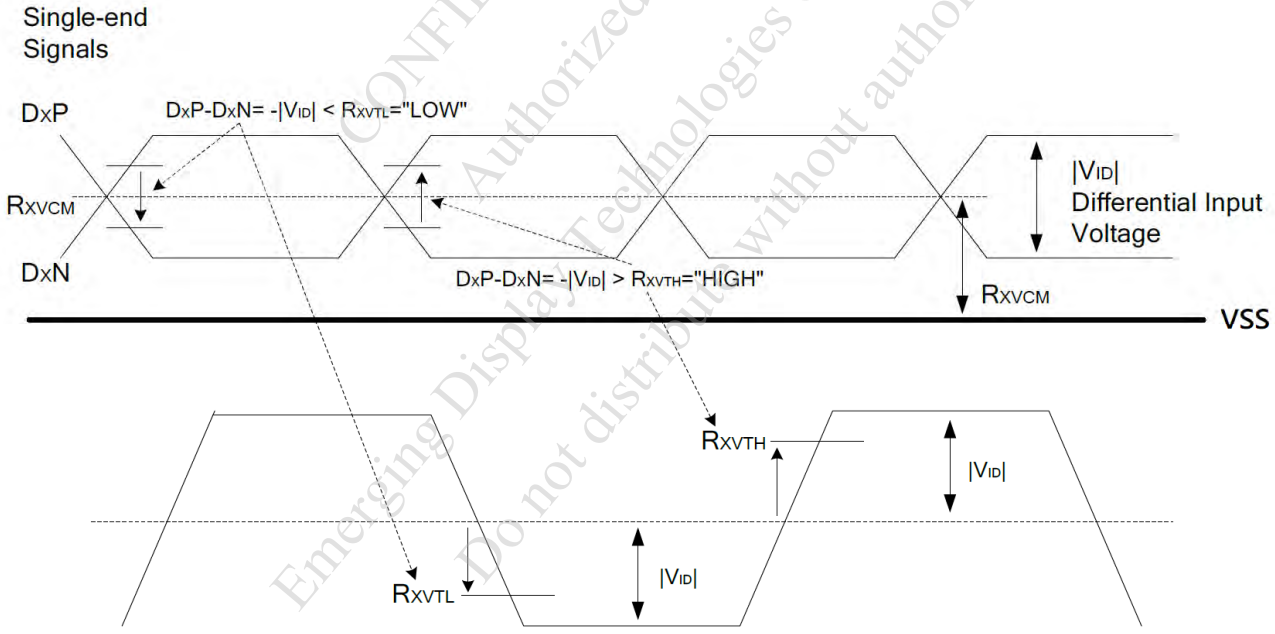
5. TIMING CHARACTERISTICS

5.1 LVDS DC ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	VALUES			UNIT	REMARK
		MIN.	TYP.	MAX.		
DIFFERENTIAL INPUT HIGH THRESHOLD VOLTAGE	R_{XVTH}	+0.1	+0.2	+0.3	V	R_{XVCM}=1.2V
DIFFERENTIAL INPUT LOW THRESHOLD VOLTAGE	R_{XVTL}	-0.3	-0.2	-0.1	V	
INPUT VOLTAGE RANGE (SINGLED-END)	R_{XVIN}	0.7	—	1.7	V	
DIFFERENTIAL INPUT COMMON MODE VOLTAGE	R_{XVCM}	1	1.2	1.4	V	$\|V_{ID}\|$=0.2
DIFFERENTIAL INPUT IMPEDANCE	Z_{ID}	80	100	125	ohm	
DIFFERENTIAL INPUT VOLTAGE	|VID|	0.2	—	0.6	V	
DIFFERENTIAL INPUT LEAKAGE CURRENT	I_{LCLVDS}	-10	—	+10	µA	
LVDS DIGITAL STAND-BY CURRENT	I_{STLVDS}	—	TBD	—	µA	CLOCK & ALL FUNCTIONS ARE STOPPED



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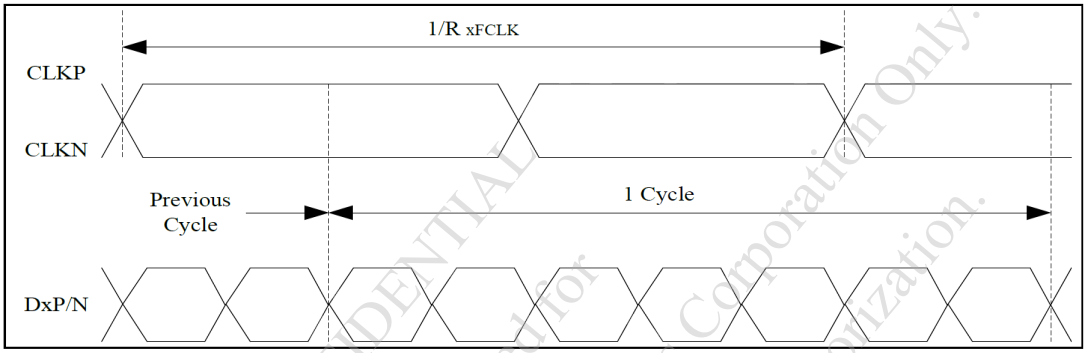
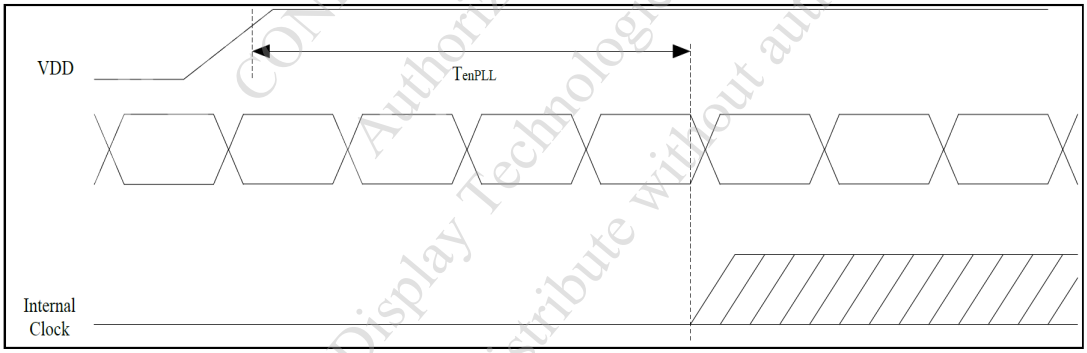
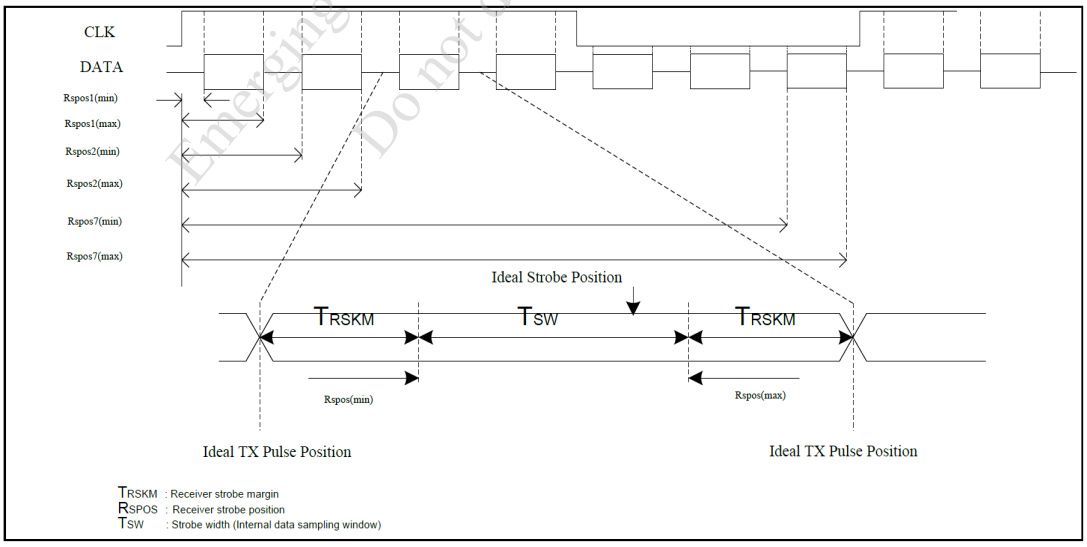
5.2 LVDS MODE AC ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	VALUES			UNIT	REMARK
		MIN.	TYP.	MAX.		
CLOCK FREQUENCY	R_{xFCLK}	30	—	75	MHz	
INPUT DATA SKEW MARGIN	T_{RSKM}	500	—	—	ps	|VID| = 200mV R_{xVCM} = 1.2V @R_{xFCLK} = 75MHz
CLOCK HIGH TIME	T_{LVCH}	—	4/(7* R_{xFCLK})	—	ns	
CLOCK LOW TIME	T_{LVCL}	—	3/(7* R_{xFCLK})	—	ns	
PLL WAKE-UP TIME	T_{enPLL}	—		150	us	







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5.3 LVDS INTERFACE

LVDS INPUT TIMING TABLE (DE MODE)

ITEM	SYMBOL	VALUES			UNIT	REMARK
		MIN.	TYP.	MAX.		
DCLK FREQUENCY	FCLK	49	51	71	MHZ	FRAME RATE =60Hz
HORIZONTAL VALID DATA	THD		1024		DCLK	
HSYNC PULSE WIDTH	THPW	6	20	NOTE 1	DCLK	
HSYNC BACK PORCH	THBP	6	20	NOTE 1	DCLK	
HSYNC FRONT PORCH	THFP	6	20	NOTE 1	DCLK	
VERTICAL VALID DATA	TVD		768		H	
VSYNC PULSE WIDTH	TVPW	1	1	NOTE 2	H	
VSYNC BACK PORCH	TVBP	8	8	NOTE 2	H	
VSYNC FRONT PORCH	TVFP	7	7	NOTE 2	H	

NOTE 1: HORIZONTAL BACK PORCH + FRONT PORCH + PULSE WIDTH < 485 TCLK WHEN V_PORCH=16

NOTE 2: VERTICAL PORCH RELATED WITH PANEL GIP TIMMING

5.4 DATA INPUT FORMAT FOR LVDS

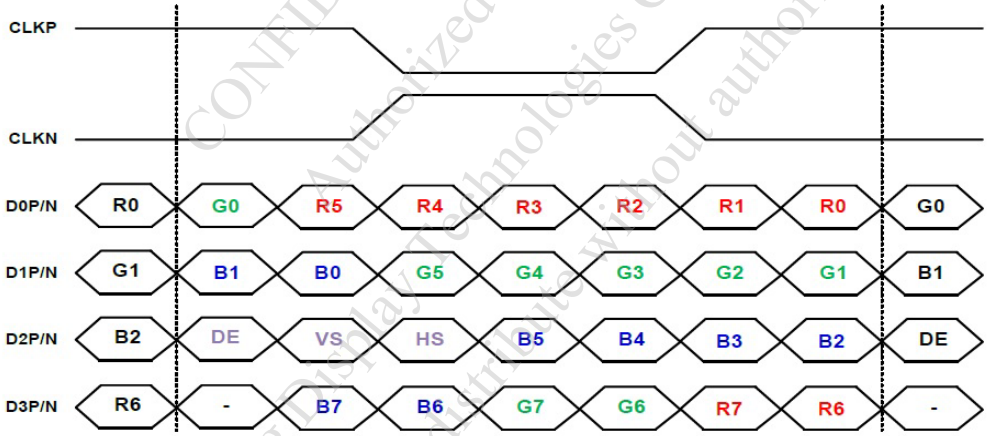

FIGURE 7.12: 8-BIT LVDS INPUT (IM[1:0]=01, LANSEL[1:0]=11, LVFMT=0(VESA))

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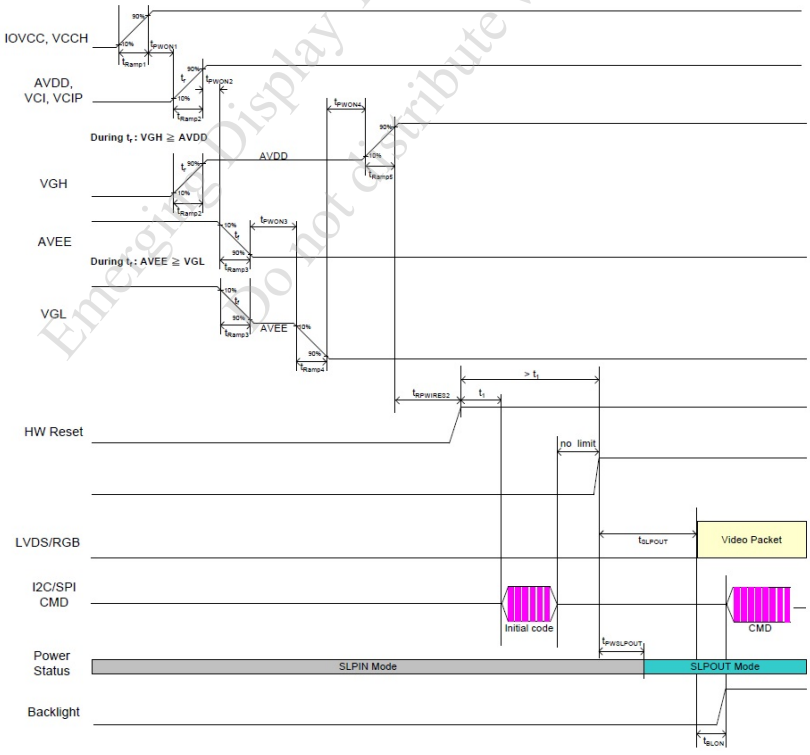
5.5 POWER ON/OFF SEQUENCE

5.5.1 POWER ON SEQUENCE

THE POWER ON SEQUENCE TIMING FOR DIFFERENT POWER INPUT MODES AND INTERFACES ARE SHOWN AS BELOW TABLE

SYMBOL	VALUES			UNIT	REMARK
	MIN.	TYP.	MAX.		
t_{PWON1}	0	5	—	MS	
t_{PWON2}	0	5	—	MS	
t_{PWON3}	0	5	—	MS	
t_{PWON4}	0	5	—	MS	
t_{ramp1}	0.2	—	20	MS	IOVCC,VCCH
t_{ramp2}	0.2	—	20	MS	VCI,VCIP,AVDD
t_{ramp3}	0.2	—	20	MS	AVEE
t_{ramp4}	0.2	—	—	MS	VGL
t_{ramp5}	0.2	—	—	MS	VGH
$t_{RPWIRES1}$	10	—	—	MS	
$t_{RPWIRES2}$	1	—	—	MS	
t_{MIPI_LP11}	—	—	$t_{RPWIRES1}$	MS	
t_{RESETL}	20	—	—	MS	
t_1	5	—	—	MS	
t_{SLPOUT}	120	—	—	MS	
$t_{PWSLPOUT}$	—	45	—	MS	
t_{BLON}	2	—	—	MS	

5 POWER MODE POWER ON SEQUENCE-VLDS,RGB:



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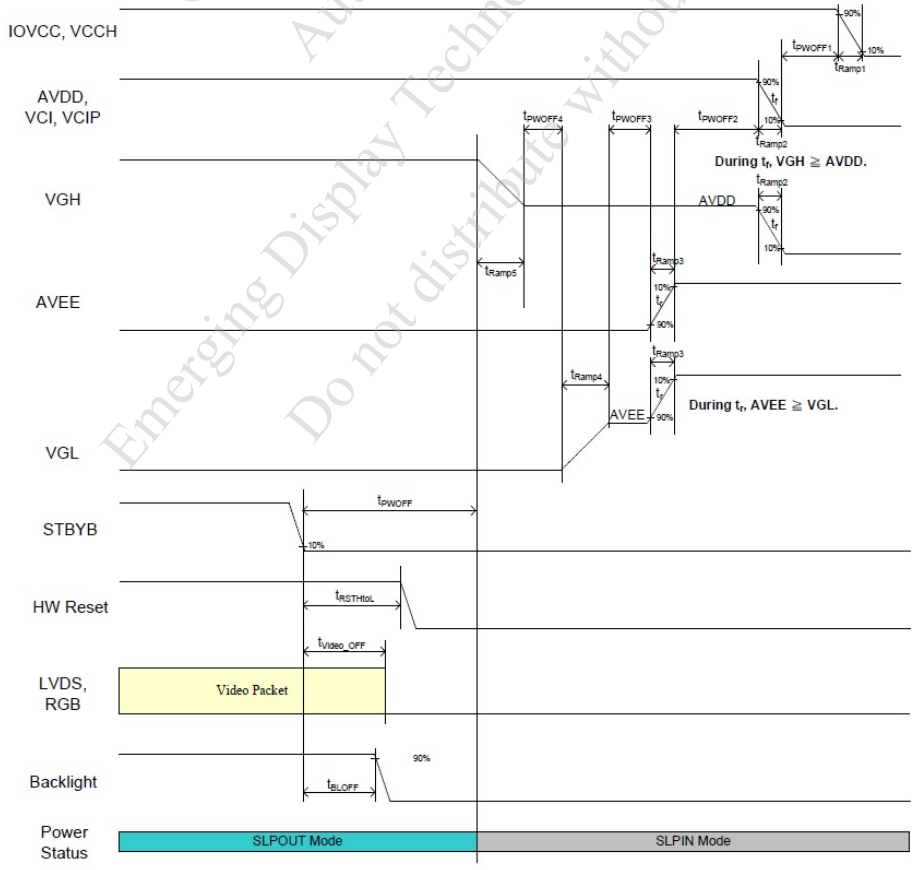
5.5.2 POWER OFF SEQUENCE

THE POWER OFF SEQUENCE TIMING FOR DIFFERENT POWER INPUT MODES AND INTERFACES ARE SHOWN AS BELOW TABLE.

SYMBOL	VALUES			UNIT	REMARK
	MIN.	TYP.	MAX.		
t_{PWOFF1}	0	5	—	MS	
t_{PWOFF2}	0	5	—	MS	
t_{PWOFF3}	0	5	—	MS	
t_{PWOFF4}	0	5	—	MS	
t_{ramp1}	0.2	—	20	MS	IOVCC,VCCH
t_{ramp2}	0.2	—	20	MS	VCI,VCIP,AVDD
t_{ramp3}	0.2	—	20	MS	AVEE
t_{ramp4}	0.2	—	—	MS	5 POWER:VGL
t_{ramp5}	0.2	—	—	MS	5 POWER:VGH
t_{PWOFF}	120	—	—	MS	
t_{MIPI_LP11}	0	—	t_{PWOFF}	MS	
$t_{DISPOFF}$	50	—	t_{PWOFF}	MS	
$t_{RSTHtoL}$	50	—	t_{PWOFF}	MS	
t_{Video_OFF}	0	—	t_{PWOFF}	MS	
t_{BLOFF}	0	—	—	MS	

5 POWER MODE POWER OFF SEQUENCE-VLDS,RGB:



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5.6 RESET INPUT TIMINGS

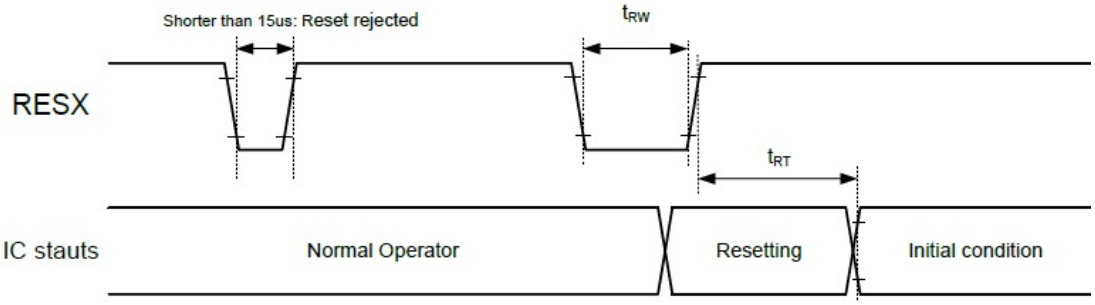

FIGURE 11.2: RESET INPUT

SYMBOL	PARAMETER	RELATED PINS	MIN.	MAX.	UNIT
t_{RW}	RESET "L" PULSE WIDTH(2)	RESX	20	—	µS
t_{RT}	RESET COMPLETE TIME(3)	—	—	5(5)	MS
		—	—	120(6)(7)(8)	MS

NOTE:(1)THE RESET COMPLETE TIME ALSO REQUIRED TIME FOR LOADING ID BYTES FROM OTP TO REGISTERS.THIS LOADING IS DONE EVERY TIME WHEN THERE IS HW RESET COMPLETE TIME (t_{RT}) WITHIN 5 MS AFTER A RISING EDGE OF RESX.
(2)SPIKE DUE TO AN ELECTROSTATIC DISCHARGE ON RESX LINE DOES NOT CAUSE IRREGULAR SYSTEM RESET ACCORDING TO THE TABLE BELOW.

RESX PULSE	ACTION
SHORTER THAN 15 µS	RESET REJECTED
LONGER THAN 20 µS	RESET
BETWEEN 15 µS AND 20 µS	RESET START

(3)DURING THE RESETTING PERIOD, THE DISPLAY WILL BE BLANKED (THE DISPLAY IS G ENTERING BLANKIN SEQUENCE, WHICH MAXIMUM TIM IS 120 MS, WHEN RESET STARTS IN SLEEP OUT –MODE. THE DISPLAY REMAINS THE BLANK STATE IN SLEEP IN –MODE) AND THEN RETURNS TO DEFAULT CONDITION FOR H/W RESET.
(4)SPIKE REJECTION ALSO APPLIES DURING A VALID RESET PULSE AS SHOWN BELOW:

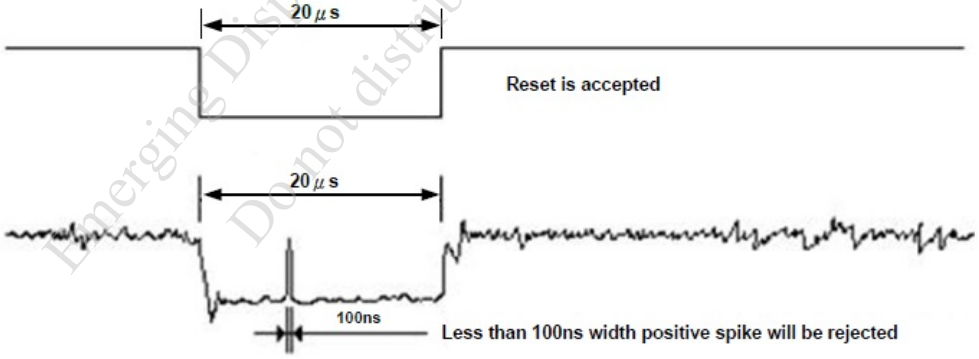

TABLE 11.4: RESET TIMINGS
(5)WHEN RESET IS APPLIED DURING SLEEP IN MODE.
(6)WHEN RESET IS APPLIED DURING SLEEP OUT MODE.
(7)IT IS NECESSARY TO WAIT 5MSEC AFTER RELEASING RESX BEFORE SENDING COMMANDS. ALSO SLEEP OUT COMMAND CANNOT BE SENT FOR 120MSEC.
(8)AFTER SLEEP OUT COMMAND, IT IS NECESSARY TO WAIT 120MSEC THEN SEND RESX.

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6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS

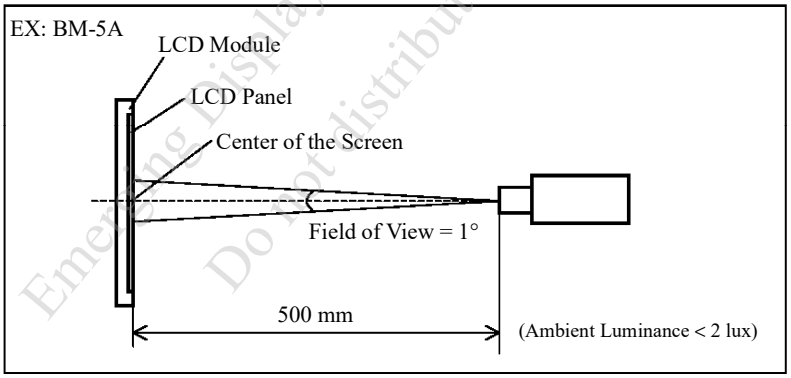
Ta=25±2°C

ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT	REMARK
VIEWING ANGLE		θy+	CR≥10	θx=0°	75	85	—	deg	NOTE (2) NOTE (3)
		θy-			75	85	—	deg	
		θx+		θy=0°	75	85	—	deg	
		θx-			75	85	—	deg	
CONTRAST RATIO (CENTER)		CR	θx=0°, θy=0°		1000	1200	—	—	NOTE (3)
RESPONSE TIME		T_R(rise)+ T_F(fall)	θx=0°, θy=0°		—	25	30	msec	NOTE (4)
COLOR CHROMATICITY (CENTER)	WHITE	Wx	θx=0°, θy=0° VDD-VSS=3.3V VBL+-VSS=12V LED B/L=ON PWM=100%		(0.27)	(0.32)	(0.37)	—	NOTE (5)
		Wy			(0.30)	(0.35)	(0.40)		
	RED	Rx			(0.59)	(0.64)	(0.69)	—	
		Ry			(0.30)	(0.35)	(0.40)		
	GREEN	Gx			(0.28)	(0.33)	(0.38)	—	
		Gy			(0.56)	(0.61)	(0.66)		
	BLUE	Bx			(0.10)	(0.15)	(0.20)	—	
		By			—	(0.05)	(0.10)		
THE BRIGHTNESS OF MODULE(CENTER)		B	—		(340)	(425)	—	cd/m^2	NOTE (6)
THE UNIFORMITY OF MODULE(9 POINTS)		—	—		70	—	—	%	NOTE (7)

NOTE (1) : TEST CONDITION :

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM.

THE MEASUREMENT CRITERIA AND CONDITION FOR EITHER BM-5A OR CA-210 OR EQUIVALENT LUMINANCE METER IS BASED ON THE EQUIPMENT'S MANUAL.



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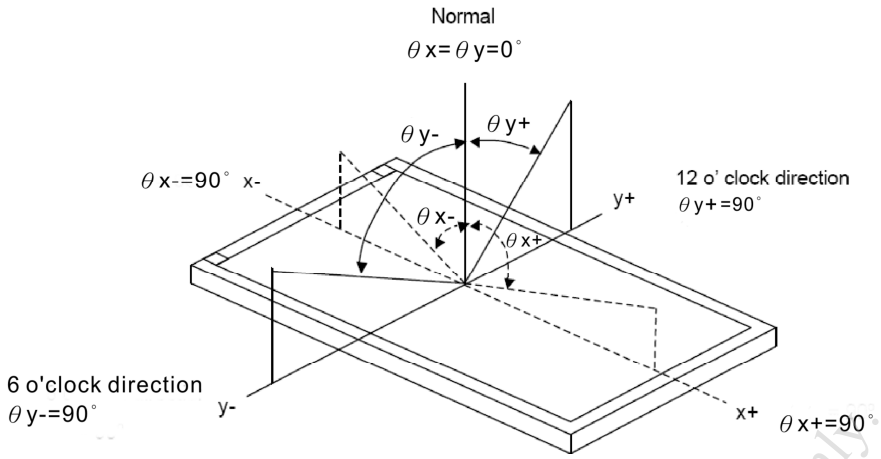
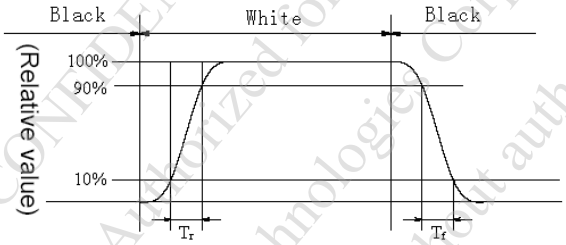
NOTE (2)：DEFINITION OF VIEWING ANGLE :



NOTE (3)：DEFINITION OF CONTRAST RATIO (CR) :
MEASURED AT THE CENTER POINT OF MODULE

$$\text{CONTRAST RATIO(CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$$

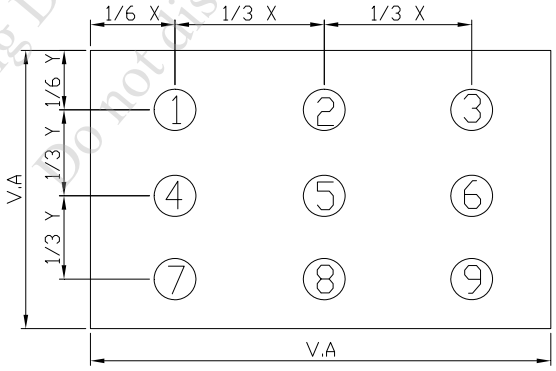
NOTE (4)：DEFINITION OF RESPONSE TIME : T_R AND T_F
THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE (5)：DEFINITION OF COLOR CHROMATICITY
(a)100% RGB PIXEL DATA TRANSMISSION WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY POWERED ON.
(b)MEASURED AT THE CENTER POINT OF MODULE

NOTE (6)：MEASURED THE BRIGHTNESS OF WHITE STATE AT CENTER POINT.

NOTE (7)：(a)DEFINITION OF BRIGHTNESS UNIFORMITY



UNIT：mm

(b)THE BRIGHTNESS UNIFORMITY CALCULATING METHOD

$$\text{UNIFORMITY}：\frac{\text{MINIMUM BRIGHTNESS}}{\text{MAXIMUM BRIGHTNESS}} *100\%$$

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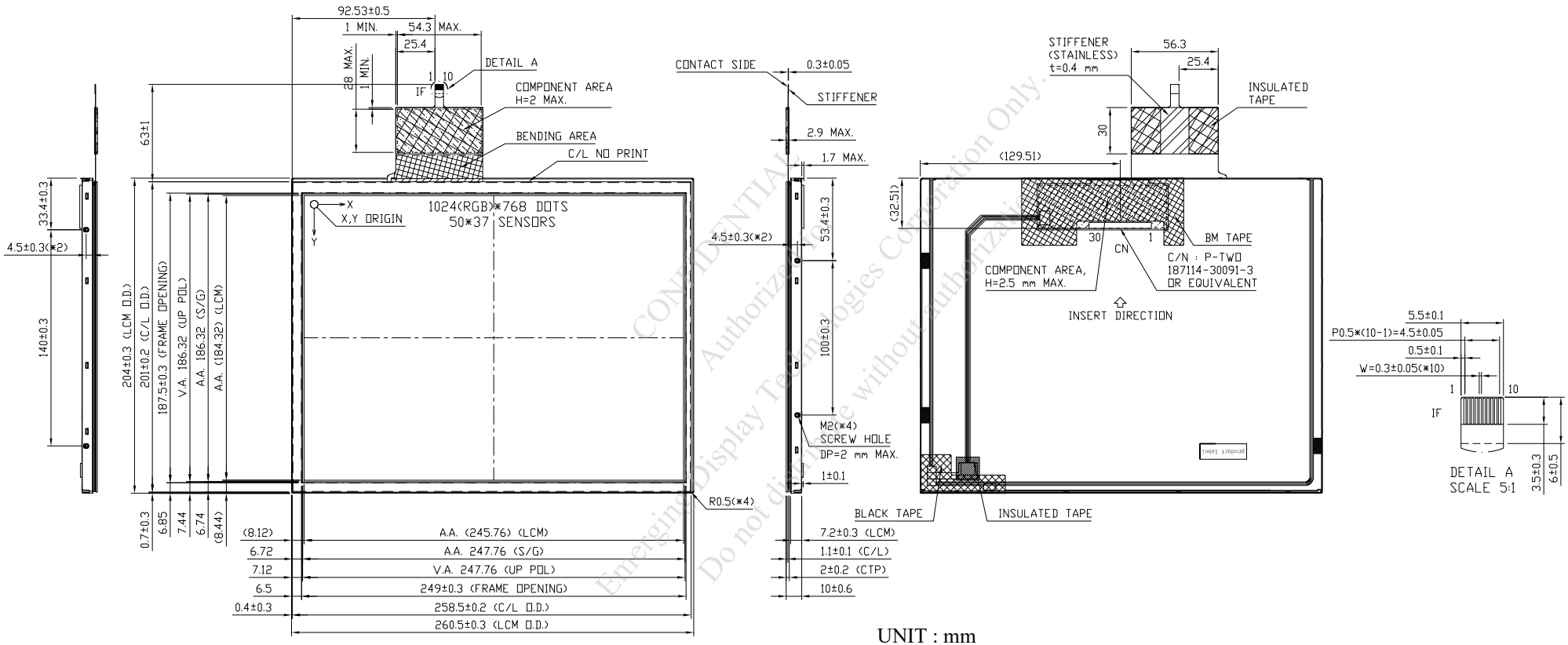
7. OUTLINE DIMENSIONS



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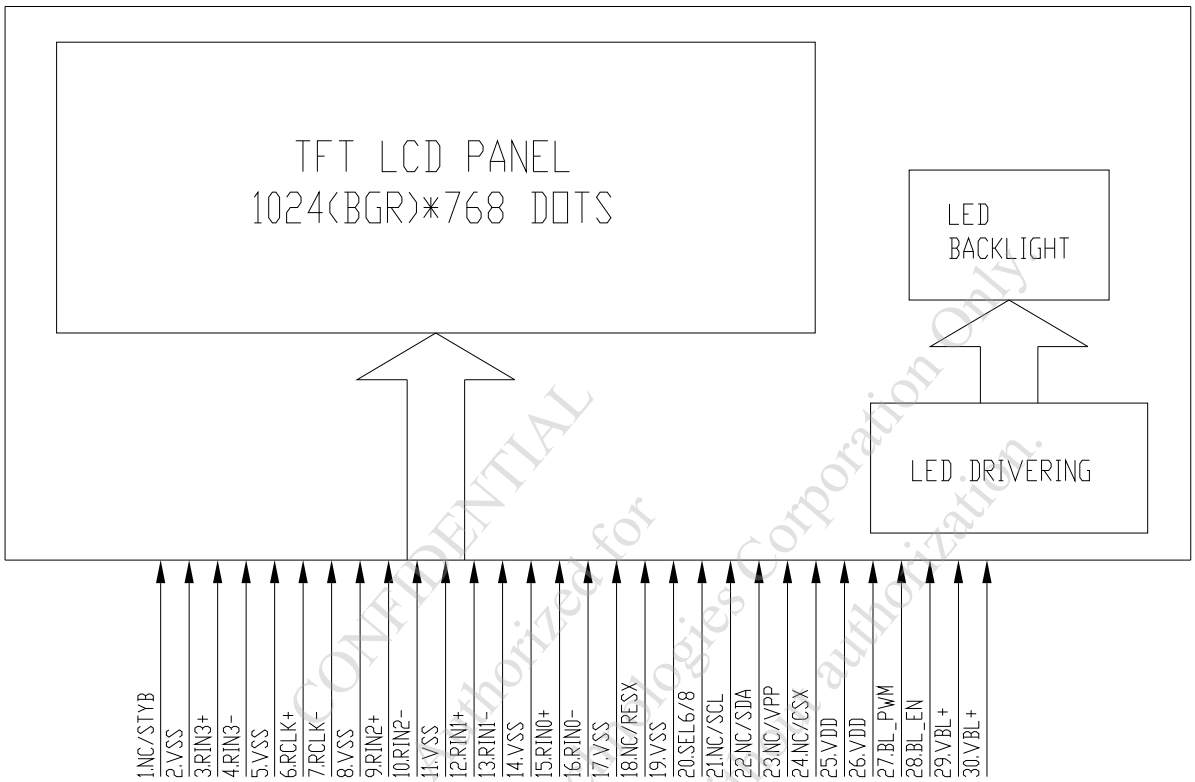
UNIT : mm
SCALE : NTS
THIRD ANGLE PROJECTION

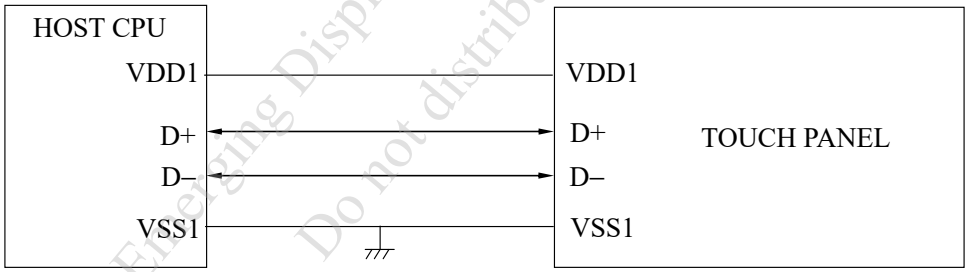
NOT SPECIFIED TOLERANCE IS ±0.5mm
1. C/L GLASS : SODA LIME, NON-STRENGTHEN, CHAMFERED EDGES.
2. FPC BENDING RADIUS SHOULD BE MORE THAN 1.0 mm.
3. ( ) : REFERENCE DIMENSION

8. BLOCK DIAGRAM

8.1 TFT LCD PANEL



8.2 CAPACITIVE TOUCH PANEL



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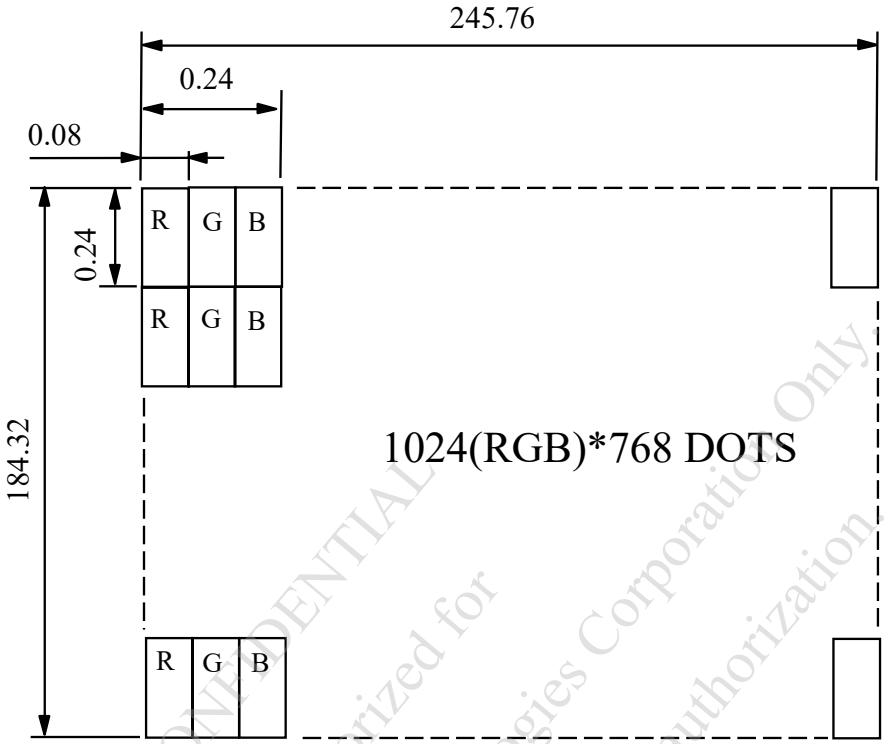
9. DETAIL DRAWING OF DOT MATRIX



UNIT : mm
SCALE : NTS
NOT SPECIFIED TOLERANCE IS ± 0.1
DOTS MATRIX TOLERANCE IS ± 0.01

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10. INTERFACE SIGNALS

10.1 TFT LCD INTERFACE

PIN NO.	SYMBOL	FUNCTION
1	NC/STBYB	THE CLIENT IS SUSPENDED AND THE MODULE IS PULLED UP
2	VSS	GROUND
3	RIN3+	+LVDS DIFFERENTIAL DATA INPUT
4	RIN3-	-LVDS DIFFERENTIAL DATA INPUT
5	VSS	GROUND
6	RCLK+	+LVDS DIFFERENTIAL CLOCK INPUT
7	RCLK-	-LVDS DIFFERENTIAL CLOCK INPUT
8	VSS	GROUND
9	RIN2+	+LVDS DIFFERENTIAL DATA INPUT
10	RIN2-	-LVDS DIFFERENTIAL DATA INPUT
11	VSS	GROUND
12	RIN1+	+LVDS DIFFERENTIAL DATA INPUT
13	RIN1-	-LVDS DIFFERENTIAL DATA INPUT
14	VSS	GROUND
15	RIN0+	+LVDS DIFFERENTIAL DATA INPUT
16	RIN0-	-LVDS DIFFERENTIAL DATA INPUT
17	VSS	GROUND
18	NC/RESX	RESET PIN. SETTING EITHER PIN LOW INITIALIZES THE LSI. MUST BE RESET (ACTIVE LOW) AFTER POWER IS SUPPLIED OR LEAVE IT OPEN FOR CUSTOMER SIDE.
19	VSS	GROUND
20	SEL6/8	SELECTION FOR 6 BITS/8BIT LVDS DATA INPUT HIGH : 8 BIT LOW : 6 BIT
21	NC/SCL	SPI CLOCK FOR OTP FOR MOULDE MAKER, OR LEAVE IT OPEN FOR CUSTOMER SIDE.
22	NC/SDA	SPI DATA FOR OTP FOR MOULDE MAKER, OR LEAVE IT OPEN FOR CUSTOMER SIDE.
23	NC/VPP	VPP USED FOR OTP FOR MOULDE MAKER, OR LEAVE IT OPEN FOR CUSTOMER SIDE.
24	NC/CSX	CHIP SELECTION (CSX) FOR OTP FOR MOULDE MAKER, OR LEAVE IT OPEN FOR CUSTOMER SIDE.
25	VDD	POWER SUPPLY: + 3.3V
26	VDD	POWER SUPPLY: + 3.3V
27	BL_PWM	PWM SIGNAL
28	BL_EN	CONVERTER POWER IC ENABLE (ACTIVE HIGH)
29	VBL+	POWER SUPPLY: + 12V
30	VBL+	POWER SUPPLY: + 12V

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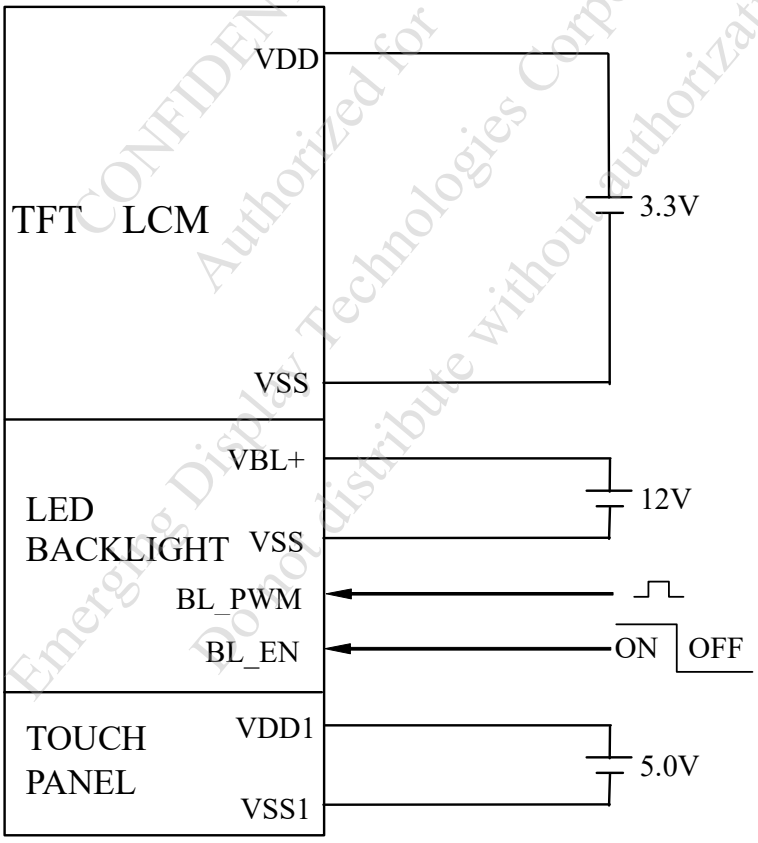
10.2 TOUCH PANEL INTERFACE

PIN NO.	SYMBOL	FUNCTION
1	NC	THE PIN WAS RESERVED FOR I2C GND
2	NC	THE PIN WAS RESERVED FOR I2C /RST
3	NC	THE PIN WAS RESERVED FOR I2C /INT
4	NC	THE PIN WAS RESERVED FOR I2C SCL
5	NC	THE PIN WAS RESERVED FOR I2C SDA
6	NC	THE PIN WAS RESERVED FOR I2C VOLTAGE
7	VDD1	POWER SUPPLY VOLTAGE
8	D–	USB D–
9	D+	USB D+
10	VSS1	GROUND

11. POWER SUPPLY

11.1 POWER SUPPLY FOR LCM



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12. CAPACITIVE TOUCH PANEL SPECIFICATION

12.1 OPTICAL CHARACTERISTICS

ITEM	CONDITION	MIN.	TYP.	MAX.	UNIT
TRANSPARENCY NOTE (1)	Ta = 25°C λ=550nm	(85)	—	—	%

NOTE (1)：OPTICAL MEASUREMENT SHOULD BE EXECUTED AFTER PANEL IS SECURED. MEASUREMENT PROCESS SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM.
OPTICAL SPECIFICATIONS SHOULD BE MEASURED BY SPECTROPHOTOMETER.

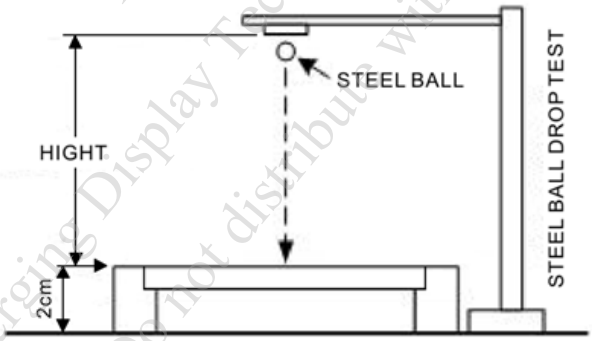
12.2 HARDNESS

ITEM	DESCRIPTION
SURFACE HARDNESS	(7)H (min)

12.3 DURABILITY

USING STEEL BALL AND FALLING ON TOUCH PANEL SURFACE, FROM THE HEIGHT MUST PASS BELOW CONDITIONS：

ITEM	CONDITION	INSPECTION METHOD	DESCRIPTION
STEEL BALL DROP TEST	WEIGHT：67g HEIGHT OF FALL：30 cm	VISUAL INSPECTION	SIGN OF FRACTURE OR DAMAGE IS NOT ACCEPTABLE 3 TIME/ 1 POINTS, 25°C(CENTER POINT)



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13. INSPECTION CRITERIA

13.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) TO CUSTOMERS

13.2 INSPECTION CONDITIONS

13.2.1 (1)OBSERVATION DISTANCE：45±5cm

(2)VIEWING ANGLE：±45°

±45° (FOR SECTION WITHIN VIEWING AREA), REFER TO FIG.A

90° (FOR SECTION OUTSIDE OF VIEWING AREA), REF TO FIG.B

PERPENDICULAR TO MODULE SURFACE

VIEWING ANGLE SHOULD BE SMALLER THAN 45°

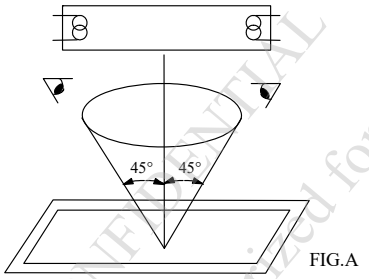

FIG.A

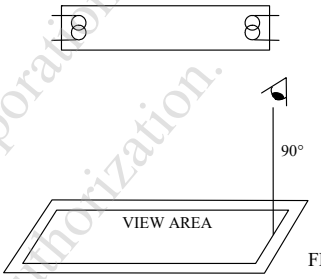

FIG.B

THE INSPECTION CRITERIA IS ACCORDING TO LINE OF SIGHT. INSPECTION SHALL BE MADE WITHIN THE HALF SECTION OF THE VIEWING CONE GENERATED BY LINE SEGMENT OF 45° WITH RESPECT TO THE VERTICAL AXIS FROM CENTER VERTEX OF LCD, THE FLUORESCENT LAMP AND THE CONE AXIS MUST BE PERPENDICULAR TO THE LCD SURFACE.

IF THE DEFECTS ARE OUTSIDE OF VIEWING AREA, IT SHALL BE INSPECTED BY 90° WITH RESPECT TO THE VERTICAL AXIS FROM EDGE OF VIEWING AREA.

13.2.2 ENVIRONMENT CONDITIONS：

AMBIENT TEMPERATURE		25±5°C
AMBIENT HUMIDITY		65 ± 20%RH
AMBIENT ILLUMINATION	COSMETIC INSPECTION	600~800 lux
	FUNCTIONAL INSPECTION	300~500 lux
INSPECTION TIME		15 secs

13.2.3 INSPECTION LOT

QUANTITY PER DELIVERY LOT FOR EACH MODEL

13.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY

(a)APPLICABLE STANDARD：

ANSI/ ASQ Z1.4 NORMAL INSPECTION LEVEL II

(b)AQL：MAJOR DEFECT：AQL 0.65

MINOR DEFECT：AQL 1.5

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13.3 INSPECTION STANDARDS

13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
MAJOR DEFECT	1.DISPLAY ON	• DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	0.65
	2.BACKLIGHT	• NO LIGHT • FLICKERING AND OTHER ABNORMAL ILLUMINATION	
	3.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
MINOR DEFECT	1.DISPLAY ZONE	• BLACK/WHITE SPOT • BUBBLES ON POLARIZER • NEWTON RING • BLACK/WHITE LINE • SCRATCH • CONTAMINATION • UNEVEN COLOR SPREAD	1.5
	2.BEZEL ZONE	• STAINS • SCRATCHES • FOREIGN MATTER	
	3.SOLDERING	• INSUFFICIENT SOLDER • SOLDERED IN INCORRECT POSITION • CONVEX SOLDERING SPOT • SOLDER BALLS • SOLDER SCRAPS	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

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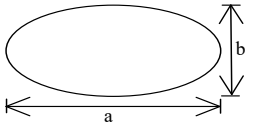
13.3.2 MODULE DEFECTS CLASSIFICATION

<table>
<tr><th>NO.</th><th>ITEM</th><th>CRITERIA</th></tr>
<tr><td>1</td><td>DISPLAY ON INSPECTION</td><td>1. INCORRECT PATTERN
2. MISSING SEGMENT
3. DIM SEGMENT
4. OPERATING VOLTAGE BEYOND SPEC</td></tr>
<tr><td>2</td><td>OVERALL DIMENSIONS</td><td>1. OVERALL DIMENSION BEYOND SPEC</td></tr>
<tr><td>3</td><td>DOT DEFECT</td><td>1. INSPECTION PATTERN: FULL WHITE, FULL BLACK, RED, GREEN AND BLUE SCREENS.
2.
<table>
<tr><th>ITEMS</th><th></th><th>ACCEPTABLE COUNT</th></tr>
<tr><td rowspan="3">BRIGHT DOT</td><td>RANDOM</td><td>N≤1</td></tr>
<tr><td>2 DOTS ADJACENT</td><td>N≤0</td></tr>
<tr><td>3 DOTS ADJACENT</td><td>N≤0</td></tr>
<tr><td>DISTANCE</td><td>MINIMUM DISTANCE BETWEEN BRIGHT DOTS</td><td>5mm</td></tr>
<tr><td rowspan="3">DARK DOT</td><td>RANDOM</td><td>N≤2</td></tr>
<tr><td>2 DOTS ADJACENT</td><td>N≤0</td></tr>
<tr><td>3 DOTS ADJACENT</td><td>N≤0</td></tr>
<tr><td colspan="2">TOTAL BRIGHT AND DARK DOT</td><td>N≤2</td></tr>
<tr><td>DISTANCE</td><td>MINIMUM DISTANCE BETWEEN DARK DOTS
MINIMUM DISTANCE BETWEEN DARK AND BRIGHT DOT.</td><td>5mm</td></tr>
<tr><td colspan="2">TINY BRIGHT DOT</td><td>VISIBLE THROUGH 5% ND FILTER
D≤0.15mm, IGNORE
0.15mm<D≤0.3mm, N≤0.3mm
DISTANCE≥5mm</td></tr>
<tr><td colspan="2">DISPLAY FAILURE (V-LINE/H-LINE/CROSS LINE ETC.)</td><td>NOT ALLOWABLE</td></tr>
<tr><td>MURA/WAVING/ HOT SPOT</td><td colspan="2">NOT VISIBLE THROUGH 5% ND FILTER</td></tr>
</table>
NOTE：
1. THE DEFINITION OF DOT：
THE SIZE OF A DEFECTIVE DOT OVER 1/2 OF WHOLE DOT IS REGARDED AS ONE DEFECTIVE DOT.
THE BRIGHT DOT DEFECT MOST BE VISIBLE THROUGH A 5% ND FILTER
2. BRIGHT DOT：
DOTS APPEAR BRIGHT AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER BLACK PATTERN.
3. DARK DOT：
DOTS APPEAR DARK AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER PURE RED, GREEN, BLUE PICTURE.</td></tr>
<tr><td>4</td><td>FOREIGN BLACK/WHITE/ BRIGHT LINE/</td><td>
<table>
<tr><th>LENGTH：L</th><th>WIDTH：W</th><th>PERMISSIBLE NO.</th></tr>
<tr><td>IGNORE</td><td>W ≤ 0.05</td><td>IGNORE</td></tr>
<tr><td>L ≤ 3.0</td><td>0.05 < W ≤ 0.1</td><td>2</td></tr>
</table>
WIDTH：W mm, LENGTH： L mm</td></tr>
<tr><td>5</td><td>FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)</td><td>
<table>
<tr><th>AVERAGE DIAMETER (mm): D</th><th>NUMBER OF PIECES PERMITTED</th></tr>
<tr><td>D ≤ 0.15</td><td>IGNORE</td></tr>
<tr><td>0.15 < D ≤ 0.4</td><td>2</td></tr>
<tr><td>0.4 < D</td><td>NONE</td></tr>
</table>
NOTE ：DIAMETER D=(a+b)/2,DISTANCE≥5mm
 
</td></tr>
</table>

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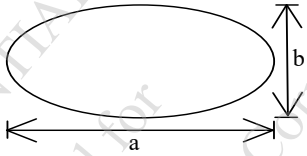
<table>
<tr><th>NO.</th><th>ITEM</th><th>CRITERIA</th></tr>
<tr><td>6</td><td>DENT/BUBBLE
ON THE POLARIZER</td><td>
<table>
<tr><th></th><th>AVERAGE DIAMETER (mm)：D</th><th>NUMBER OF PIECES PERMITTED</th></tr>
<tr><td rowspan="3">BUBBLE ON THE POLARIZER</td><td>D ≤ 0.15</td><td>IGNORE</td></tr>
<tr><td>0.15 < D ≤ 0.4</td><td>N ≤ 2</td></tr>
<tr><td>0.4 < D</td><td>NONE</td></tr>
<tr><td rowspan="2">SCRATCHES</td><td>W ≤ 0.05</td><td>IGNORE</td></tr>
<tr><td>0.05 < W ≤ 0.1 , L≤3</td><td>N ≤ 2</td></tr>
</table>
NOTE : (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA.
(2)THE EXTRANEOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON.
(3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING.
AVERAGE DIAMETER (D)=(a+b)/2,DISTANCE≥5mm
 

</td></tr>
<tr><td>7</td><td>LINE DEFECT ON DISPLAY</td><td>OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOWED</td></tr>
<tr><td>8</td><td>MURA ON DISPLAY</td><td>MURA NOT VISIBLE THROUGH 5% ND FILTER</td></tr>
<tr><td>9</td><td>UNEVEN COLOR SPREAD, COLORATION</td><td>1. TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.</td></tr>
<tr><td>10</td><td>BEZEL APPEARANCE</td><td>1. BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION.
2. BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.</td></tr>
<tr><td>11</td><td>PCB</td><td>1. THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES.
2. NO OXIDATION OR CONTAMINATION PCB TERMINALS.
3. PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART.
THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS.
4. THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART.
5. IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD; MAKE SURE IT IS SMOOTHED DOWN.</td></tr>
</table>

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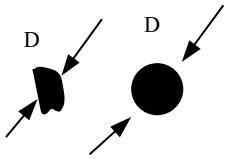
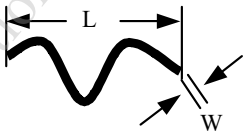
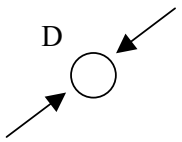
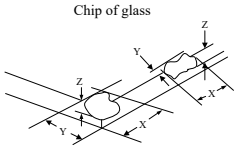
<table>
<tr><th>NO.</th><th>ITEM</th><th>CRITERIA</th><th>REMARK</th></tr>
<tr><td>12</td><td>BLACK/WHITE SPOT CIRCULAR TYPE</td><td>THE FOLLOWING BLACK/WHITE SPOT ARE WITHIN THE VIEWING AREA.
AVERAGE DIAMETER：D (mm)
<table><tr><th>SIZE D</th><th>PERMISSIBLE NO.</th></tr><tr><td>D≤0.3mm</td><td>IGNORE</td></tr><tr><td>0.3mm<D≤0.5mm</td><td>9</td></tr><tr><td>D>0.5 mm</td><td>0</td></tr></table>NOTE (1)：THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.</td><td>  </td></tr>
<tr><td>13</td><td>SCRATCH</td><td>THE FOLLOWING BLACK LINE, WHITE LINE IS WITHIN THE VIEWING AREA.
WIDTH：W (mm) , LENGTH : L (mm)
<table><tr><th>SIZE W & L</th><th>PERMISSIBLE NO.</th></tr><tr><td>W≤0.07mm</td><td>IGNORE</td></tr><tr><td>0.07 mm<W≤0.1mm , L>10mm</td><td>6</td></tr><tr><td>W>0.1mm</td><td>0</td></tr></table>NOTE (1)：THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.</td><td rowspan="2">  </td></tr>
<tr><td>14</td><td>BLACK/WHITE LINE LINEAR TYPE/FOREIGN FIBER</td><td>THE FOLLOWING BLACK LINE, WHITE LINE IS WITHIN THE VIEWING AREA.
WIDTH：W (mm) , LENGTH : L (mm)
<table><tr><th>SIZE W & L</th><th>PERMISSIBLE NO.</th></tr><tr><td>W≤0.07mm</td><td>IGNORE</td></tr><tr><td>0.07 mm<W≤0.1mm , L>10mm</td><td>6</td></tr><tr><td>W>0.1mm</td><td>0</td></tr></table>NOTE (1)：THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.</td></tr>
<tr><td>15</td><td>BUBBLE / DENT FOR OPTICAL BONDING</td><td>BUBBLES WITHIN VIEWING AREA.
AVERAGE DIAMETER：D (mm)
<table><tr><th>SIZE D</th><th>PERMISSIBLE NO.</th></tr><tr><td>D≤0.3mm</td><td>IGNORE</td></tr><tr><td>0.3mm<D≤0.5mm</td><td>9</td></tr><tr><td>D>0.5 mm</td><td>0</td></tr></table></td><td>  </td></tr>
<tr><td>16</td><td>CHIP DAMAGE ON GLASS</td><td><table><tr><td>CORNER</td><td>X ≤ 3mm、Y ≤ 3mm、Z ≤ t
(t : thickness)</td></tr><tr><td>EDGE</td><td>X≤6 mm、Y≤ 1mm、Z <t
(t : thickness)</td></tr></table></td><td>  </td></tr>
<tr><td>17</td><td>CRACK</td><td colspan="2">NOT ACCEPTABLE</td></tr>
<tr><td>18</td><td>LINE DEFECT ON DISPLAY</td><td colspan="2">OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOWED</td></tr>
<tr><td>19</td><td>MURA ON DISPLAY</td><td colspan="2">MURA NOT VISIBLE THROUGH 5% ND FILTER</td></tr>
<tr><td>20</td><td>UNEVEN COLOR SPREAD, COLORATION</td><td colspan="2">1. TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.</td></tr>
</table>

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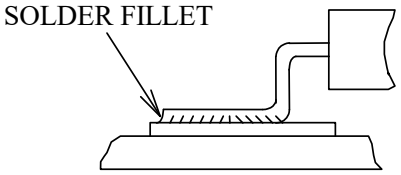
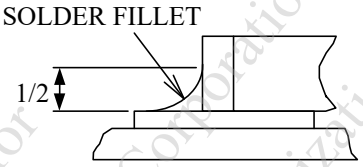
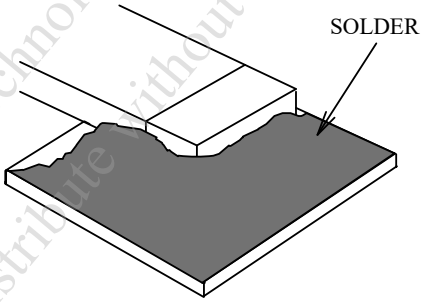
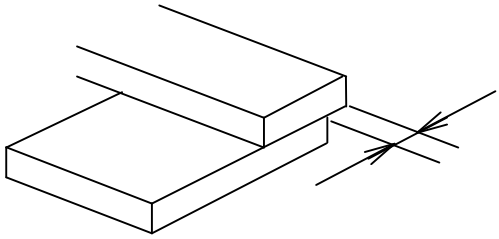
NO.	ITEM	CRITERIA
21	SOLDERING	1.NO SOLDERING FOUND ON THE SPECIFIED PLACE 2.INSUFFICIENT SOLDER (a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD  (b)CHIP COMPONENT · SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING  · SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED  3.PARTS ALIGNMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE 

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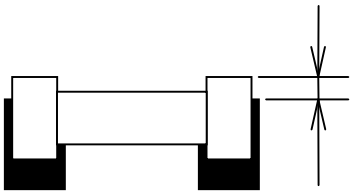
NO.	ITEM	CRITERIA
21	SOLDERING	(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE  4.NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. 5.NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. 6.NO RESIDUE OR SOLDER BALLS ON PCB. 7.NO SHORT CIRCUITS IN COMPONENTS ON PCB.
22	BACKLIGHT	1.NO LIGHT 2.FLICKERING AND OTHER ABNORMAL ILLUMINATION 3.SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. 4.BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
23	GENERAL APPEARANCE	1.NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. 2.NO CRACKS ON INTERFACE PIN (OLB) OF TCP. 3.NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. 4.THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. 5.THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. 6.THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. 7.SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. 8.PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. 9.LCD PIN LOOSE OR MISSING PINS. 10.PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. 11.PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. 12.THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

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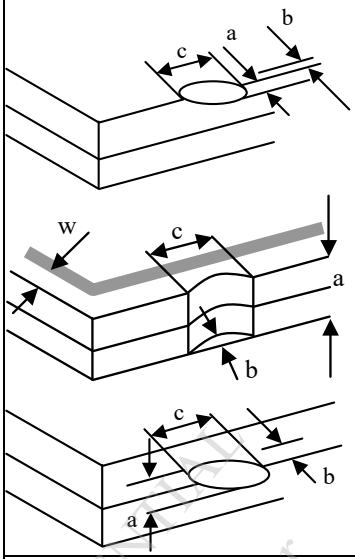
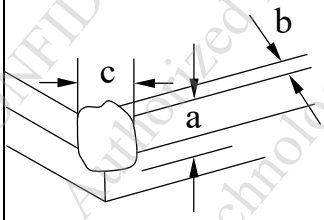
<table>
<tr><th>NO.</th><th>ITEM</th><th>CRITERIA</th></tr>
<tr><td rowspan="4">24</td><td rowspan="4">CRACKED GLASS</td><td colspan="2">THE LCD WITH EXTENSIVE CRACK IS NOT ACCEPTABLE</td></tr>
<tr><td>GENERAL GLASS CHIP：
 
</td><td>
<table>
<tr><th>a</th><th>b</th><th>c</th></tr>
<tr><td>≤ t/2</td><td>< VIEWING AREA</td><td>≤ 1/8X</td></tr>
<tr><td>t/2 > , ≤ 2t</td><td>≤ W/2</td><td>≤ 1/8X</td></tr>
</table>
*W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE
X = LCD SIDE LENGTH
t = GLASS THICKNESS
</td></tr>
<tr><td>CORNER PART：
 
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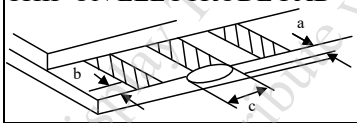
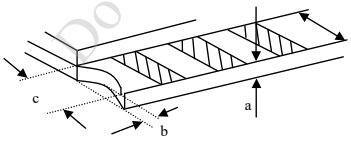
<tr><th>a</th><th>b</th><th>c</th></tr>
<tr><td>≤ t/2</td><td>< VIEWING AREA</td><td>≤ 1/8X</td></tr>
<tr><td>> t/2 , ≤ 2t</td><td>≤ W/2</td><td>≤ 1/8X</td></tr>
</table>
*W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE
X = LCD SIDE LENGTH
t = GLASS THICKNESS
</td></tr>
<tr><td>CHIP ON ELECTRODE PAD
 
</td><td>
<table>
<tr><th>a</th><th>b</th><th>c</th></tr>
<tr><td>≤ t</td><td>≤ 0.5mm</td><td>≤ 1/8X</td></tr>
</table>
* X=LCD SIDE WIDTH
t =GLASS THICKNESS
</td></tr>
<tr><td> 
</td><td>
<table>
<tr><th>a</th><th>b</th><th>c</th></tr>
<tr><td>≤ t</td><td>≤ 1/8X</td><td>≤ L</td></tr>
</table>
*X=LCD SIDE WIDTH
t = GLASS THICKNESS
L=ELECTRODE PAD LENGTH
①IF GLASS CHIPPING THE ITO TERMINAL, OVER 2/3 OF THE ITO MUST REMAIN AND BE, INSPECTED ACCORDING TO ELECTRODE TERMINAL SPECIFICATIONS
②IF THE PRODUCT WILL BE HEAT SEALED BY THE CUSTOMER, THE ALIGNMENT MARK MUST NOT BE DEMAGED
</td></tr>
</table>

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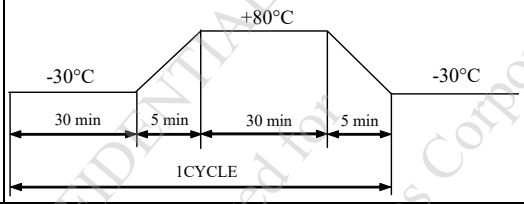
14. RELIABILITY TEST

14.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION
1	HIGH TEMPERATURE TEST (OPERATION)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
2	LOW TEMPERATURE TEST (OPERATION)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
3	HIGH TEMPERATURE TEST (STORAGE)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
4	LOW TEMPERATURE TEST (STORAGE)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HIGH TEMPERATURE / HUMIDITY TEST (OPERATION)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH 240 HRS
6	THERMAL SHOCK TEST (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: 
7	VIBRATION (NOT OPERATED)	11.76 m/s2(1.2G), 10~100 Hz XYZ DIRECTIONS, 1 HR EACH
8	SHOCK (NOT OPERATED)	490.0 m/s2(50G), 10 ms XYZ DIRECTIONS, 1 TIME EACH
9	ESD (ELECTROSTATIC DISCHARGE) (NOT OPERATED)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV (ACCORDING TO IEC-61000-4-2)

NOTE (1) : THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

NOTE (2) : THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED, ALL THE COSMETIC SPECIFICATION IS JUDGED BEFORE THE RELIABILITY STRESS.

NOTE (3) : THE MODULE SHOULDN'T BE TESTED MORE THAN ONE CONDITION, AND ALL THE TEST CONDITIONS ARE INDEPENDENT.

NOTE (4) : TESTING CONDITIONS AND INSPECTION CRITERIA

NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

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15. CAUTION

15.1 OPERATION

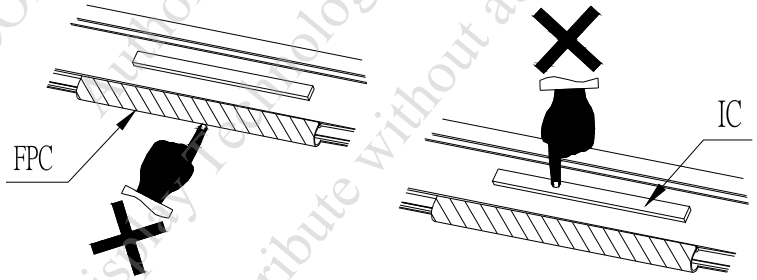
15.1.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .

15.1.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE ; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY ; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY .

15.1.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST.

15.1.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE .
IF ABOVE SEQUENCE IS NOT FOLLOWED , CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH - UP PROBLEM .

15.1.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!
DO NOT STRESS FPC AND IC ON THE MODULE!



15.2 STORAGE

15.2.1 STORE THE MODULE IN A DARK ROOM OR KEEP IN ORIGINAL PACKAGE WHERE MUST KEEP AT 25±10°C AND 65%RH OR LESS.

15.2.2 DO NOT STORE THE MODULE IN SURROUNDINGS CONTAINING ORGANIC SOLVENT OR CORROSIVE GAS.

15.2.3 STORE THE MODULE IN AN ANTI-ELECTROSTATIC CONTAINER OR BAG.

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15.3 NOTICE

15.3.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THETHROUGH-HOLE-PAD .

15.3.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .

15.3.3 DO NOT CHARGE STATIC ELECTRICITY , AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL .

15.3.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE ; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE .

15.3.5 DON’T GIVE EXTERNAL SHOCK.

15.3.6 DON’T APPLY EXCESSIVE FORCE ON THE SURFACE.

15.3.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.
WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC.
WASH IT OUT THOROUGHLY AND IMMEDIATELY.

15.3.8 DON’T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.

15.3.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS AND SOLVENT.

15.3.10 STORE WITHOUT ANY PHYSICAL LOAD.

15.3.11 REWIRING: NO MORE THAN 3 TIMES.

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