

EXAMINED BY :  <i>Yung Chang Hu</i>	<b>EMERGING DISPLAY</b>  TECHNOLOGIES CORPORATION	FILE NO . CAS-51582
APPROVED BY:		ISSUE : DEC.27, 2007
<i>David Chang</i>		TOTAL PAGE : 23
		VERSION : 4

CUSTOMER                      ACCEPTANCE                      SPECIFICATIONS

MODEL NO. :

ET057005DMU

(RoHS)

FOR MESSRS :

\_\_\_\_\_

CUSTOMER'S APPROVAL

DATE :

\_\_\_\_\_

BY :

\_\_\_\_\_

RECORDS OF REVISION

DOC . FIRST ISSUE

APR.04, 2007

DATE

REVISED  
PAGE  
NO.

SUMMARY

OCT.01,2007

2

3. 1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT	REMARK
POWER VOLTAGE	VDD	-0.1	+12	V	VSS=0
	VCC	-0.1	+12	V	VSS=0
	VGH	-0.1	+2	V	VSS=0
INPUT VOLTAGE	VGE	VGH+2	+9.3	V	VSS=0
	V <sub>i</sub>	-0.1	VDD+0.1	V	VSS=0

PARAMETER	SYMBOL	MIN	MAX	UNIT	REMARK
POWER VOLTAGE	VDDA	-0.1	+13	V	VSSA=0V
	VDD	-0.1	+12	V	VSS=0
	VGE	VGH+2	+9.3	V	VSS=0
INPUT SIGNAL VOLTAGE	V <sub>i</sub>	-0.1	VDD+0.1	V	VSS=0
LED BACKLIGHT DIMENSION	JD	—	1.28	W	—
LED BACKLIGHT CURRENT	IF	—	0.06	A	—
LED BACKLIGHT REVERSE VOLTAGE	VR	—	40	V	—

3

4. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REMARK
DIGITAL POWER SUPPLY	VCC	—	(2.7)	(3.3)	(3.6)	V	—
	VDD0	—	(2.7)	(3.3)	(3.6)	V	—
ANALOG POWER SUPPLY	VDDA	L LEVEL	(6.5)	(8.4)	(13.5)	V	—
GATE ON CURRENT	IGH	H LEVEL	—	(0.00)	—	mA	—
GATE OFF CURRENT	IGL	L LEVEL	—	(0.00)	—	mA	—
VCOM VOLTAGE	VCOM	—	(1)	—	—	V	—
FRAME FREQUENCY	f <sub>FRAME</sub>	—	60	90	—	Hz	—
FORWARD VOLTAGE	V <sub>f</sub>	I <sub>f</sub> =40mA	(28)	(30)	(32)	V	—

PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REMARK
DIGITAL POWER SUPPLY	VCC	—	2.7	3.3	3.6	V	—
ANALOG POWER SUPPLY	VDDA	L LEVEL	—	(11.6)	(13.5)	V	—
VCOM VOLTAGE	VCOM	—	(1)	—	—	V	—
INPUT GAMMA VOLTAGE	V1-V4	—	(0.2)	—	(10)	—	—
FRAME FREQUENCY	f <sub>FRAME</sub>	—	90	60	72	Hz	NOTE (1)
FORWARD VOLTAGE	V <sub>f</sub>	I <sub>f</sub> =40mA	28	30	32	V	—

ADD NOTE ( 1 )

4, 5

5. TIMING CHART

UPDATE 5.1 DIGITAL PARALLEL RGB INTERFACE (SYNC MODE)  
ADD 5.2 DIGITAL PARALLEL RGB INTERFACE (DE MODE)  
ADD 5.3 SPI TIMING CHARACTERISTICS

6, 7

6.1 OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REMARK
VIEWING ANGLE	θ <sub>v</sub>	CR ≥ 10	0°	(55)	(62)	—	4 × 4 (5)
	θ <sub>h</sub>	—	—	(60)	(67)	—	—
	θ <sub>v</sub>	θ <sub>h</sub> = 0°	—	—	—	—	—
CONTRAST RATIO	CR	θ <sub>v</sub> = 0°, θ <sub>h</sub> = 0°	(280)	(300)	—	—	(3)
RESPONSE TIME	t <sub>RT</sub> (10%)	θ <sub>v</sub> = 0°, θ <sub>h</sub> = 0°	—	(15)	(30)	ms	(2)
THE BRIGHTNESS OF MODULE	B	θ <sub>v</sub> = 0°, θ <sub>h</sub> = 0°	(150)	(400)	—	cd/m <sup>2</sup>	—
	WHTL	—	(0.20)	(0.11)	(0.36)	—	(4)
COLOR OF CR COORDINATE	RED	θ <sub>v</sub> = 0°, θ <sub>h</sub> = 0°	(0.50)	(0.64)	(0.69)	—	—
	GRN	—	(0.10)	(0.30)	(0.47)	—	—
	BLU	—	(0.10)	(0.11)	(0.35)	—	—
	BLU	—	(0.07)	(0.14)	(0.37)	—	—

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REMARK
VIEWING ANGLE	θ <sub>v</sub>	CR ≥ 10	0°	55	—	—	(2)
	θ <sub>h</sub>	—	—	62	—	—	(3)
	θ <sub>v</sub>	θ <sub>h</sub> = 0°	—	60	65	—	—
CONTRAST RATIO	CR	θ <sub>v</sub> = 0°, θ <sub>h</sub> = 0°	280	300	—	—	(3)
RESPONSE TIME	t <sub>RT</sub> (10%)	θ <sub>v</sub> = 0°, θ <sub>h</sub> = 0°	—	15	30	ms	(4)
THE BRIGHTNESS OF MODULE	B	θ <sub>v</sub> = 0°, θ <sub>h</sub> = 0°	150	400	—	cd/m <sup>2</sup>	(5)
	WHTL	—	0.20	0.11	0.36	—	(6)
COLOR OF CR COORDINATE	RED	θ <sub>v</sub> = 0°, θ <sub>h</sub> = 0°	0.50	0.64	0.69	—	—
	GRN	—	0.10	0.30	0.47	—	—
	BLU	—	0.10	0.11	0.35	—	—
	BLU	—	0.07	0.14	0.37	—	—

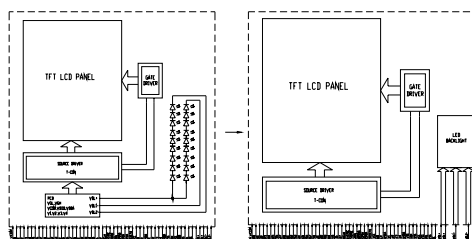
UPDATE NOTE(1) ~ NOTE (5)

ADD NOTE ( 6 )

UPDATE 6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY

9

8. BLOCK DIAGRAM



11

10. INTERFACE SIGNALS

PIN NO	SYMBOL	IO	FUNCTION
9	VDD0	P	DIGITAL POWER

↓

PIN NO	SYMBOL	IO	FUNCTION
9	VCC	P	DIGITAL POWER

RECORDS OF REVISION

DOC . FIRST ISSUE

APR.04, 2007

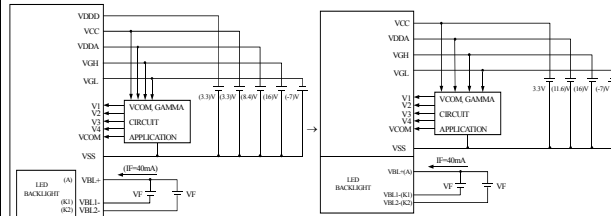
DATE

REVISED  
PAGE  
NO.

SUMMARY

13

1 1.1 POWER SUPPLY FOR LCM



ADD 11.2 ~ 11.3

OCT.15, 2007

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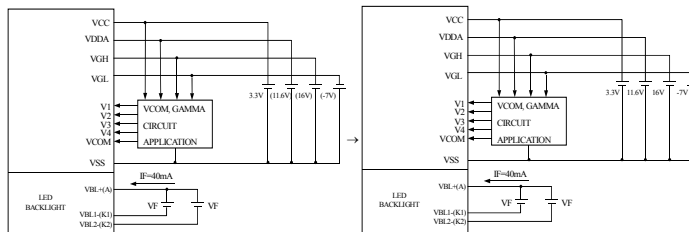
4. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
DIGITAL OPERATING CURRENT	ICC	H LEVEL	—	(1)	(11)	mA	
ANALOG POWER SUPPLY	VDDA	L LEVEL	—	(11.6)	(13.5)	V	
ANALOG OPERATING CURRENT	IDDA	—	—	(5)	(9)	mA	
GATE ON POWER	VGH	H LEVEL	—	(16)	—	V	
GATE OFF POWER	VGL	L LEVEL	—	(-7)	—	V	
VCOM VOLTAGE	VCOM	—	—	(3)	—	V	
INPUT GAMMA VOLTAGE	V1-V4	—	(0.2)	—	(10)	—	

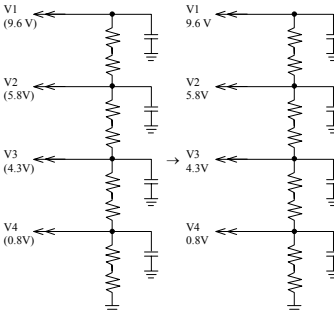
PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
DIGITAL OPERATING CURRENT	ICC	H LEVEL	—	8	11	mA	
ANALOG POWER SUPPLY	VDDA	L LEVEL	—	11.6	13.5	V	
ANALOG OPERATING CURRENT	IDDA	—	—	17	20	mA	
GATE ON POWER	VGH	H LEVEL	15.5	16	16.5	V	
GATE OFF POWER	VGL	L LEVEL	-7.5	-7	-6.5	V	
VCOM VOLTAGE	VCOM	—	—	3	3.5	V	
INPUT GAMMA VOLTAGE	V1-V4	—	0.2	—	10	—	

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1 1.1 POWER SUPPLY FOR LCM



1 1.2 GAMMA CIRCAIT → GAMMA CIRCUIT



DEC.27, 2007

1

2. MECHANICAL SPECIFICATIONS

- (1) DISPLAY SIZE (inch) : 5.7" → DIAGONALS : 5.7 inch
- (4) ACTIVE AREA : 115.2W \* 86.4H mm (LCD) → 115.2W \* 86.4H mm
- (9) COLOR : 16.7M (24 BIT) → 16.7M, ADD (12) INTERFACE MODE

3

4. ELECTRICAL CHARACTERISTICS

- LED LIFE TIME → LED CHIP LIFETIME
- FORWARD VOLTAGE → LED BACKLIGHT FORWARD VOLTAGE
- ADD NOTE (2) AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT (PER LED)

RECORDS OF REVISION APR.04, 2007

DATE	REVISED PAGE NO.	SUMMARY
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DEC.27, 2007	6	<p>6.1 OPTICAL CHARACTERISTICS</p> <table border="1"> <thead> <tr> <th>ITEM</th> <th>SYMBOL</th> <th>CONDITION</th> <th>MIN.</th> <th>TYP.</th> <th>MAX.</th> <th>UNIT</th> <th>REMARK</th> </tr> </thead> <tbody> <tr> <td rowspan="2">RESPONSE TIME</td> <td><math>t_r</math> (rise)</td> <td><math>\theta_x=0^\circ, \theta_y=0^\circ</math></td> <td>—</td> <td>15</td> <td>30</td> <td rowspan="2">msec</td> <td rowspan="2">(4)</td> </tr> <tr> <td><math>t_f</math> (fall)</td> <td><math>\theta_x=0^\circ, \theta_y=0^\circ</math></td> <td>—</td> <td>35</td> <td>50</td> </tr> <tr> <td>THE BRIGHTNESS OF MODULE</td> <td>B</td> <td><math>\theta_x=0^\circ, \theta_y=0^\circ</math> IF=40mA</td> <td>400</td> <td>450</td> <td>—</td> <td>cd/m<sup>2</sup></td> <td>(5)</td> </tr> <tr> <td rowspan="6">COLOR OF CIE COORDINATE</td> <td rowspan="2">WHITE</td> <td>x</td> <td>0.26</td> <td>0.31</td> <td>0.36</td> <td rowspan="2">—</td> <td rowspan="2">(6)</td> </tr> <tr> <td>y</td> <td>0.30</td> <td>0.35</td> <td>0.40</td> </tr> <tr> <td rowspan="2">RED</td> <td>x</td> <td>0.56</td> <td>0.61</td> <td>0.66</td> <td rowspan="2">—</td> <td rowspan="2">—</td> </tr> <tr> <td>y</td> <td>0.31</td> <td>0.36</td> <td>0.41</td> </tr> <tr> <td rowspan="2">GREEN</td> <td>x</td> <td>0.28</td> <td>0.33</td> <td>0.38</td> <td rowspan="2">—</td> <td rowspan="2">—</td> </tr> <tr> <td>y</td> <td>0.51</td> <td>0.56</td> <td>0.61</td> </tr> <tr> <td rowspan="2">BLUE</td> <td>x</td> <td>0.09</td> <td>0.14</td> <td>0.19</td> <td rowspan="2">—</td> <td rowspan="2">—</td> </tr> <tr> <td>y</td> <td>0.07</td> <td>0.12</td> <td>0.17</td> </tr> <tr> <td>THE UNIFORMITY OF MODULE</td> <td>—</td> <td>—</td> <td>75</td> <td>80</td> <td>—</td> <td>%</td> <td>—</td> </tr> </tbody> </table> <table border="1"> <thead> <tr> <th>ITEM</th> <th>SYMBOL</th> <th>CONDITION</th> <th>MIN.</th> <th>TYP.</th> <th>MAX.</th> <th>UNIT</th> <th>REMARK</th> </tr> </thead> <tbody> <tr> <td rowspan="2">RESPONSE TIME</td> <td><math>t_r</math> (rise)</td> <td><math>\theta_x=0^\circ, \theta_y=0^\circ</math></td> <td>—</td> <td>15</td> <td>30</td> <td rowspan="2">msec</td> <td rowspan="2">(4)</td> </tr> <tr> <td><math>t_f</math> (fall)</td> <td><math>\theta_x=0^\circ, \theta_y=0^\circ</math></td> <td>—</td> <td>35</td> <td>50</td> </tr> <tr> <td>THE BRIGHTNESS OF MODULE</td> <td>B</td> <td><math>\theta_x=0^\circ, \theta_y=0^\circ</math> IF=40mA</td> <td>400</td> <td>450</td> <td>—</td> <td>cd/m<sup>2</sup></td> <td>(5)</td> </tr> <tr> <td rowspan="6">COLOR OF CIE COORDINATE</td> <td rowspan="2">WHITE</td> <td><math>W_x</math></td> <td>0.26</td> <td>0.31</td> <td>0.36</td> <td rowspan="2">—</td> <td rowspan="2">(6)</td> </tr> <tr> <td><math>W_y</math></td> <td>0.30</td> <td>0.35</td> <td>0.40</td> </tr> <tr> <td rowspan="2">RED</td> <td><math>R_x</math></td> <td>0.56</td> <td>0.61</td> <td>0.66</td> <td rowspan="2">—</td> <td rowspan="2">—</td> </tr> <tr> <td><math>R_y</math></td> <td>0.31</td> <td>0.36</td> <td>0.41</td> </tr> <tr> <td rowspan="2">GREEN</td> <td><math>G_x</math></td> <td>0.28</td> <td>0.33</td> <td>0.38</td> <td rowspan="2">—</td> <td rowspan="2">—</td> </tr> <tr> <td><math>G_y</math></td> <td>0.51</td> <td>0.56</td> <td>0.61</td> </tr> <tr> <td rowspan="2">BLUE</td> <td><math>B_x</math></td> <td>0.09</td> <td>0.14</td> <td>0.19</td> <td rowspan="2">—</td> <td rowspan="2">—</td> </tr> <tr> <td><math>B_y</math></td> <td>0.07</td> <td>0.12</td> <td>0.17</td> </tr> <tr> <td>THE UNIFORMITY OF MODULE</td> <td>—</td> <td>IF=40mA</td> <td>75</td> <td>80</td> <td>—</td> <td>%</td> <td>—</td> </tr> </tbody> </table>	ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK	RESPONSE TIME	$t_r$ (rise)	$\theta_x=0^\circ, \theta_y=0^\circ$	—	15	30	msec	(4)	$t_f$ (fall)	$\theta_x=0^\circ, \theta_y=0^\circ$	—	35	50	THE BRIGHTNESS OF MODULE	B	$\theta_x=0^\circ, \theta_y=0^\circ$ IF=40mA	400	450	—	cd/m <sup>2</sup>	(5)	COLOR OF CIE COORDINATE	WHITE	x	0.26	0.31	0.36	—	(6)	y	0.30	0.35	0.40	RED	x	0.56	0.61	0.66	—	—	y	0.31	0.36	0.41	GREEN	x	0.28	0.33	0.38	—	—	y	0.51	0.56	0.61	BLUE	x	0.09	0.14	0.19	—	—	y	0.07	0.12	0.17	THE UNIFORMITY OF MODULE	—	—	75	80	—	%	—	ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK	RESPONSE TIME	$t_r$ (rise)	$\theta_x=0^\circ, \theta_y=0^\circ$	—	15	30	msec	(4)	$t_f$ (fall)	$\theta_x=0^\circ, \theta_y=0^\circ$	—	35	50	THE BRIGHTNESS OF MODULE	B	$\theta_x=0^\circ, \theta_y=0^\circ$ IF=40mA	400	450	—	cd/m <sup>2</sup>	(5)	COLOR OF CIE COORDINATE	WHITE	$W_x$	0.26	0.31	0.36	—	(6)	$W_y$	0.30	0.35	0.40	RED	$R_x$	0.56	0.61	0.66	—	—	$R_y$	0.31	0.36	0.41	GREEN	$G_x$	0.28	0.33	0.38	—	—	$G_y$	0.51	0.56	0.61	BLUE	$B_x$	0.09	0.14	0.19	—	—	$B_y$	0.07	0.12	0.17	THE UNIFORMITY OF MODULE	—	IF=40mA	75	80	—	%	—
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7	<p>6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY</p> <p>6.3 THE CALCULATING METHOD OF UNIFORMITY</p> <p>UNIFORMITY: <math>\left[ 1 - \frac{\text{MAXIMUM BRIGHTNESS} - \text{MINIMUM BRIGHTNESS}}{\text{AVERAGE BRIGHTNESS}} \right] \times 100\% \rightarrow</math></p> <p>UNIFORMITY: <math>\left[ 1 - \frac{\text{MAXIMUM BRIGHTNESS} - \text{MINIMUM BRIGHTNESS}}{\text{AVERAGE BRIGHTNESS}} \right] \times 100\%</math></p>
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8	7. OUTLINE DIMENSIONS MARK $\Delta$ : ADDING NOTE
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13	<p>11. POWER SUPPLY</p>
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16~20	12.3.2 MODULE DEFECTS CALSSIFICATION REVISING THE ENTIRE PAGE
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21	<p>12.4 RELIABILITY TEST</p> <p>NO.1 : +60°C → +70°C, NO.2 : -10°C → -20°C, NO.3 : +70°C → +80°C</p> <p>NO.4 : -20°C → 30°C, NO.5 ADD STORAGE</p> <p>NO.6 : 200 CYCLES OF OPERATION → 10 CYCLES OF OPERATION</p> <p>-25°C for 30 MINUTES ~ +70°C for 30 MINUTES →</p> <p>-40°C for 30 MINUTES ~ +85°C for 30 MINUTES</p> <p>NO.7 : ADD (NOT OPERATED)</p>
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22	ADD 12.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!
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1. GENERAL SPECIFICATIONS

1.1 APPLICATION NOTES FOR CONTROLLER/DRIVER  
PLEASE REFER TO :

H I M A X H X 8 2 5 0  
H I M A X H X 8 6 7 8

1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE)

2. MECHANICAL SPECIFICATIONS

- (1) DIAGONALS ----- 5.7 inch
- (2) NUMBER OF DOTS ----- 640W \* (RGB) \* 480H DOTS
- (3) MODULE SIZE ----- 147.6W \* 100.0H \* 6.0D mm  
(WITHOUT FPC)
- (4) EFFECTIVE AREA ----- 117.2W \* 88.4H mm
- (5) ACTIVE AREA ----- 115.2W \* 86.4H mm
- (6) DOT SIZE ----- 0.06W \* 0.18H mm
- (7) PIXEL SIZE ----- 0.18W \* 0.18H mm
- (8) LCD TYPE ----- TFT , TRANSMISSIVE
- (9) COLOR ----- 16.7M
- (10) VIEWING DIRECTION ----- 12 O'CLOCK
- (11) BACK LIGHT ----- LED , COLOR : WHITE
- (12) INTERFACE MODE ----- RGB, 24 BIT, PARALLEL (DE/SYNC MODE)

### 3. ABSOLUTE MAXIMUM RATINGS

#### 3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS .

VSS = VSSA = 0V

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER VOLTAGE	VDDA	-0.3	+13.5	V	
	VCC	-0.3	7.0	V	
	VGH	-0.3	42	V	
	VGL	VGH-42	+0.3	V	
INPUT VOLTAGE	Vi	- 0.3	VCC+0.3	V	
LED BACKLIGHT DISSIPATION	PD	—	1.28	W	
LED BACKLIGHT CURRENT	IF	—	0.06	A	
LED BACKLIGHT REVERSE VOLTAGE	VR	—	45	V	

#### 3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS .

I T E M	OPERATING		STORAGE		REMARK
	MIN.	MAX.	MIN.	MAX.	
AMBIENT TEMPERATURE	- 2 0 °C	7 0 °C	- 3 0 °C	8 0 °C	NOTE ( 1 ) , ( 2 )
HUMIDITY	NOTE ( 3 )		NOTE ( 3 )		WITHOUT CONDENSATION
VIBRATION	—	2.45 m/s <sup>2</sup> ( 0.25 G)	—	11.76 m/s <sup>2</sup> ( 1.2 G)	5~20Hz , 1HR 20~500Hz(20Hz) , 1HR 20~500Hz(500Hz) , 1HR X,Y,Z,TOTAL 3HR
SHOCK	—	29.4 m/s <sup>2</sup> ( 3 G)	—	490 m/s <sup>2</sup> ( 5 0 G)	10 m SECONDS XYZ DIRECTIONS 1 TIME EACH
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE		

NOTE ( 1 ) : Ta AT -30°C : 48HR MAX .  
80°C : 168HR MAX .

NOTE ( 2 ) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE  
THIS PHENOMENON IS REVERSIBLE.

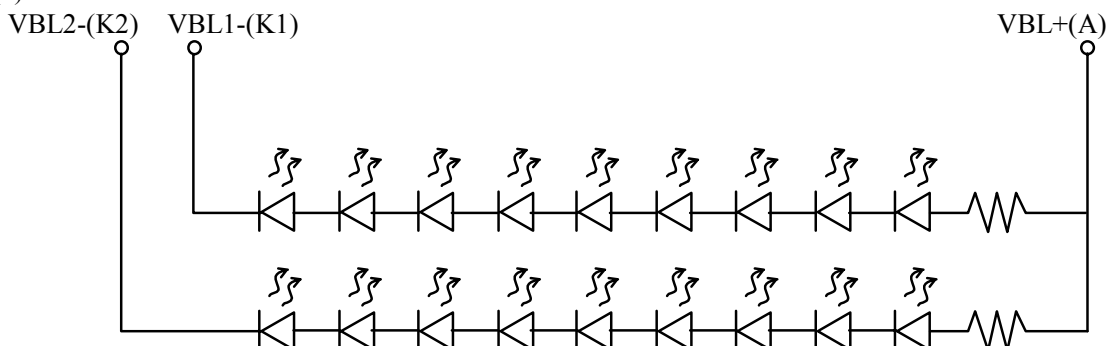
NOTE ( 3 ) : Ta ≤ 60°C : 90%RH MAX (96HRS MAX).  
Ta > 60°C : ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY  
OF 90%RH AT 60°C(96HRS MAX).

4. ELECTRICAL CHARACTERISTICS

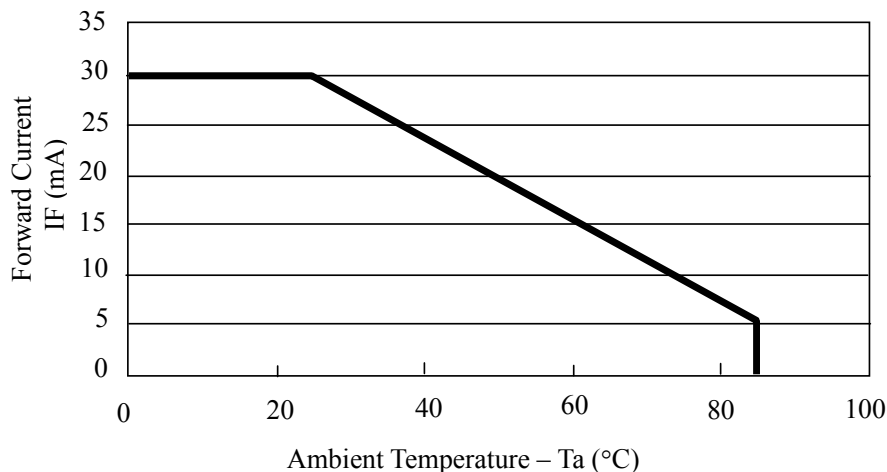
Ta = 25 °C

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
DIGITAL POWER SUPPLY	VCC	—	2.7	3.3	3.6	V	
DIGITAL OPERATING CURRENT	ICC	H LEVEL	—	8	11	mA	
ANALOG POWER SUPPLY	VDDA	L LEVEL	—	11.6	13.5	V	
ANALOG OPERATING CURRENT	IDDA	—	—	17	20	mA	
GATE ON POWER	VGH	H LEVEL	15.5	16	16.5	V	
GATE OFF POWER	VGL	L LEVEL	-7.5	-7	-6.5	V	
VCOM VOLTAGE	VCOM	—	—	3	3.5	V	
INPUT GAMMA VOLTAGE	V1~V4	—	0.2	—	10	—	
FRAME FREQUENCY	fFRAME	—	50	60	72	Hz	
LED BACKLIGHT FORWARD VOLTAGE	V <sub>F</sub>	I <sub>F</sub> =40mA	28	30	32	V	NOTE ( 1 )
LED CHIP LIFETIME	—	—	30000	40000	—	hr	

NOTE (1) : INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



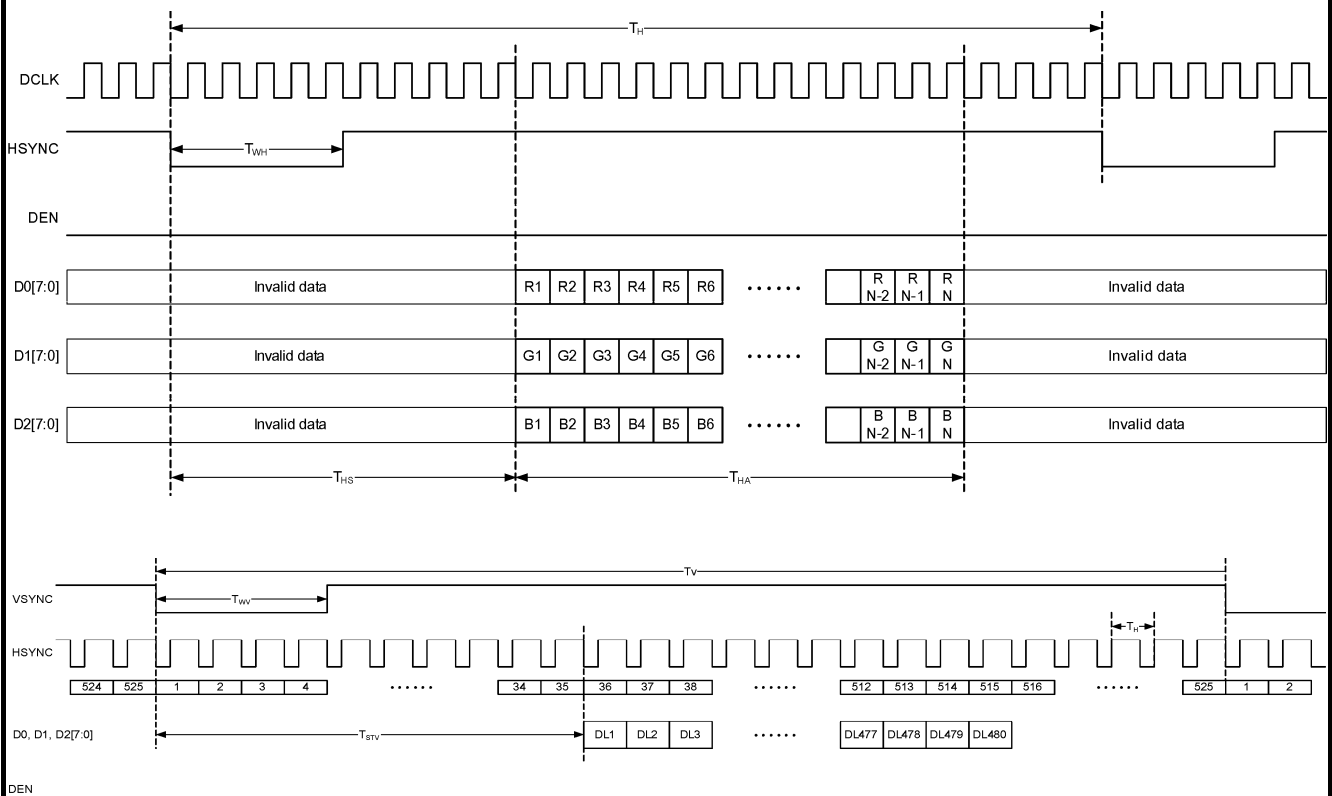
NOTE ( 2 ) : AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT. (PER LED)



5. TIMING CHART

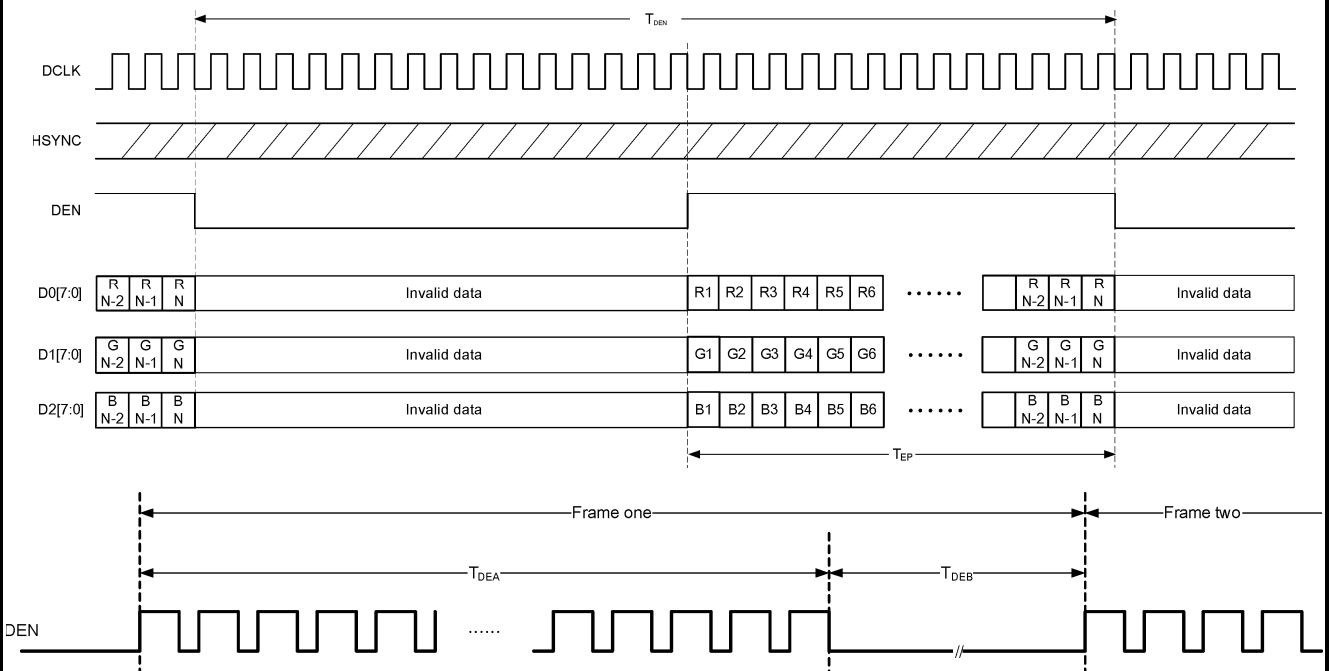
5.1 DIGITAL PARALLEL RGB INTERFACE (SYNC MODE)

PARAMETER	SYMBOL	SPEC.			UNIT
		MIN.	TYP.	MAX.	
DCLK FREQUENCY	$F_{CPH}$	22.66	25.175	27.69	MHZ
DCLK PERIOD	$T_{CPH}$	36.11	39.7	44.13	ns
DCLK PULSE DUTY	$T_{CWH}$	40	50	60	%
HSYNC PERIOD	$T_H$	750	800	850	$T_{CPH}$
HSYNC PULSE WIDTH	$T_{WH}$	5	30	—	$T_{CPH}$
HSYNC-FIRST HORIZONTAL DATA TIME	$T_{HS}$	112	144	175	$T_{CPH}$
HORIZONTAL ACTIVE DATA AREA	$T_{HA}$	—	640	—	$T_{CPH}$
VSYNC PULSE WIDTH	$T_{WV}$	1	3	5	$T_H$
FIRST LINE DATA INPUT TIME	$T_{STV}$	—	35	—	$T_H$
VSYNC PERIOD	$T_V$	515	525	535	$T_H$



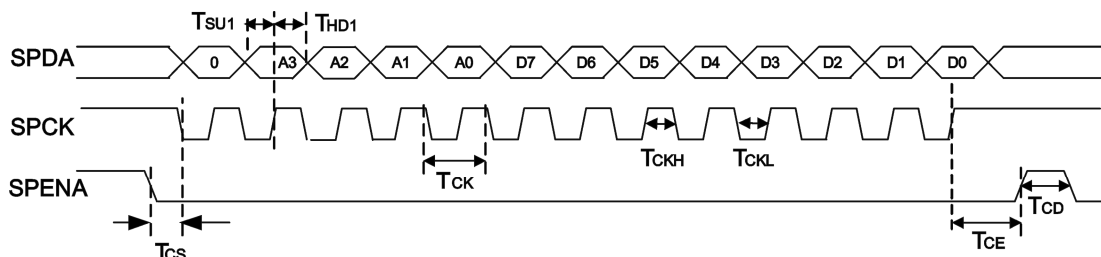
5.2 DIGITAL PARALLEL RGB INTERFACE (DE MODE)

PARAMETER	SYMBOL	SPEC.			UNIT
		MIN.	TYP.	MAX.	
DCLK FREQUENCY	$F_{CPH}$	22.66	25.175	27.69	MHZ
DCLK PERIOD	$T_{CPH}$	36.11	39.7	44.13	ns
DCLK PULSE DUTY	$T_{CWH}$	40	50	60	%
DEN PERIOD	$T_{DEN}$	750	800	850	$T_{CPH}$
DEN PULSE WIDTH	$T_{EP}$	—	640	—	$T_{CPH}$
DEN FRAME ACTIVE TIME	$T_{DEA}$	—	480	—	$T_H$
DEN FRAME BLANKING TIME	$T_{DEB}$	10	45	110	$T_H$



5.3 SPI TIMING CHARACTERISTICS

PARAMETER	SYMBOL	SPEC.			UNIT
		MIN.	TYP.	MAX.	
SPCK PERIOD	$T_{CK}$	60	—	—	ns
SPCK HIGH WIDTH	$T_{CKH}$	30	—	—	ns
SPCK LOW WIDTH	$T_{CKL}$	30	—	—	ns
DATA SETUP TIME	$T_{SU1}$	12	—	—	ns
DATA HOLD TIME	$T_{HD1}$	12	—	—	ns
SPENA TO SPCK SETUP TIME	$T_{CS}$	20	—	—	ns
SPENA TO SPDA HOLD TIME	$T_{CE}$	20	—	—	ns
SPENA HIGH PULSE WIDTH	$T_{CD}$	50	—	—	ns



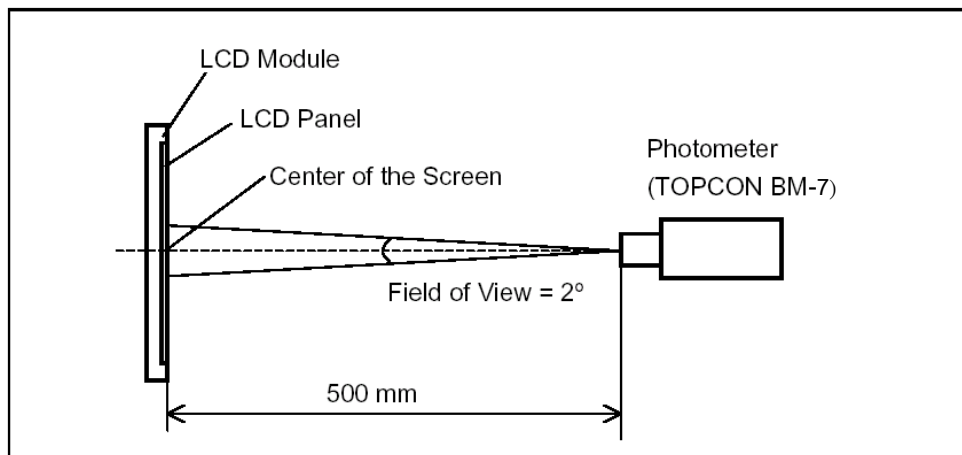
6. OPTICAL CHARACTERISTICS (NOTE 1)  
6.1 OPTICAL CHARACTERISTICS

Ta = 25 ± 2 °C

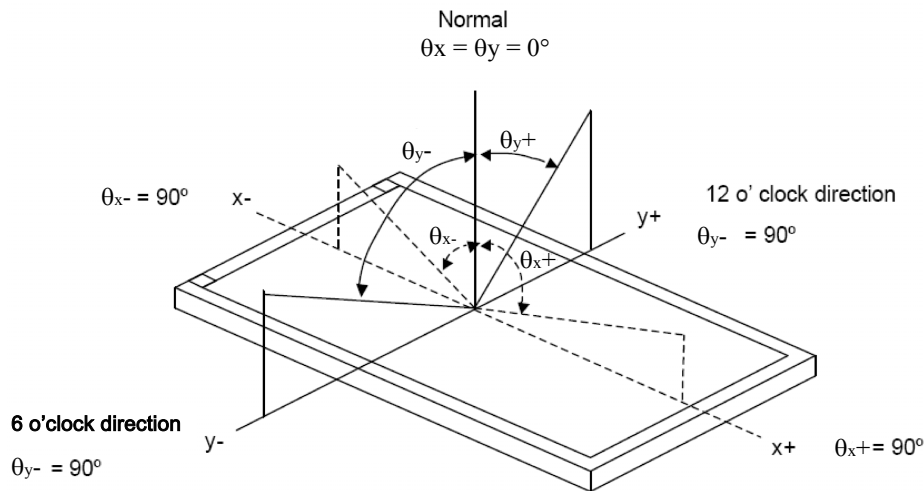
I T E M		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK	
VIEWING ANGLE		$\theta_{y+}$	CR ≥ 10	50	55	—	deg.	(2) (3)	
		$\theta_{y-}$		$\theta_x=0^\circ$	47	52			—
		$\theta_{x+}$		$\theta_y=0^\circ$	60	65			—
		$\theta_{x-}$			60	65			—
CONTRAST RATIO		CR	$\theta_x=0^\circ, \theta_y=0^\circ$	300	350	—	—	(3)	
RESPONSE TIME		T <sub>R</sub> ( rise )	$\theta_x=0^\circ, \theta_y=0^\circ$	—	15	30	msec	(4)	
		T <sub>F</sub> ( fall )		—	35	50			
THE BRIGHTNESS OF MODULE		B	$\theta_x=0^\circ, \theta_y=0^\circ$ IF=40mA	400	450	—	cd/m <sup>2</sup>	(5)	
COLOR OF CIE COORDINATE	WHITE	W <sub>X</sub>	$\theta_x=0^\circ, \theta_y=0^\circ$ IF=40mA NTSC:50%	0.26	0.31	0.36	—	(6)	
		W <sub>Y</sub>		0.30	0.35	0.40			
	RED	R <sub>X</sub>		0.56	0.61	0.66	—		
		R <sub>Y</sub>		0.31	0.36	0.41			
	GREEN	G <sub>X</sub>		0.28	0.33	0.38	—		
		G <sub>Y</sub>		0.51	0.56	0.61			
	BLUE	B <sub>X</sub>		0.09	0.14	0.19	—		
		B <sub>Y</sub>		0.07	0.12	0.17			
THE UNIFORMITY OF MODULE		—	IF=40mA	75	80	—	%	—	

NOTE (1) : TEST EQUIPMENT SETUP :

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 2° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.



NOTE (2) : DEFINITION OF VIEWING ANGLE :

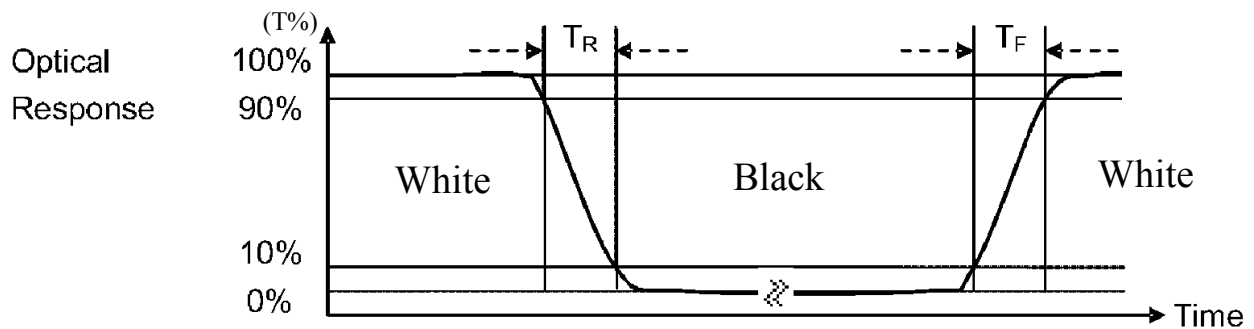


NOTE (3) : DEFINITION OF CONTRAST RATIO :

$$\text{CONTRAST RATIO (CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$$

NOTE (4) : DEFINITION OF RESPONSE TIME : TR AND TF

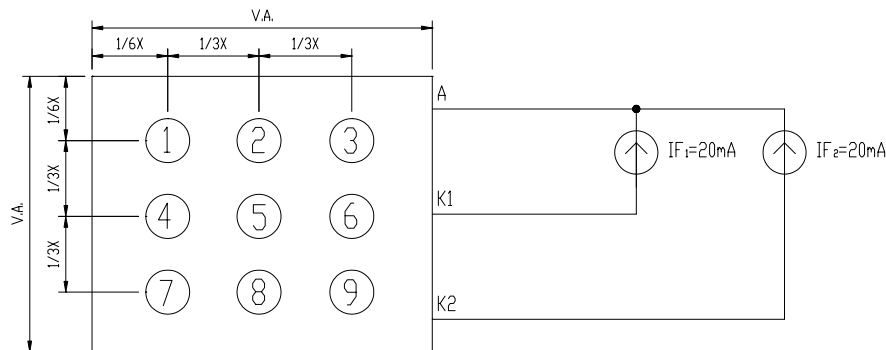
THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE (5) : BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"

NOTE (6) : THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

## 6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY

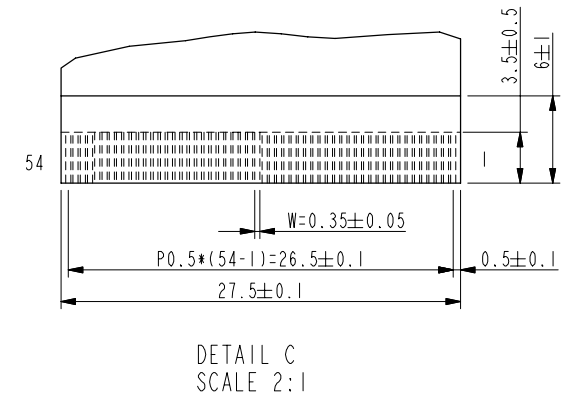
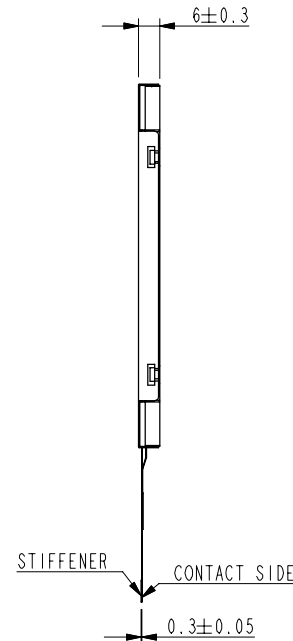
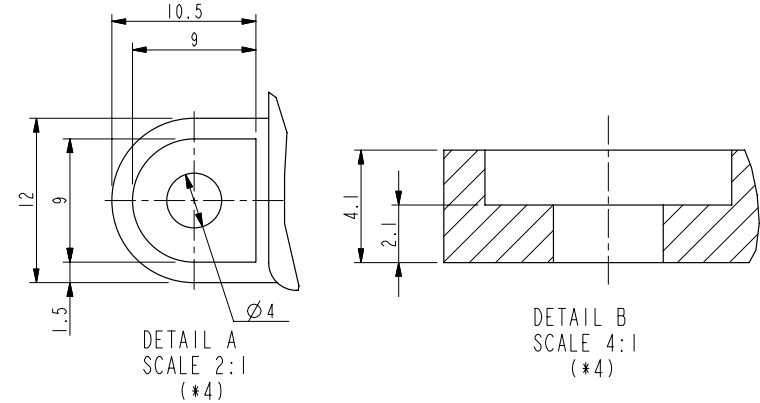
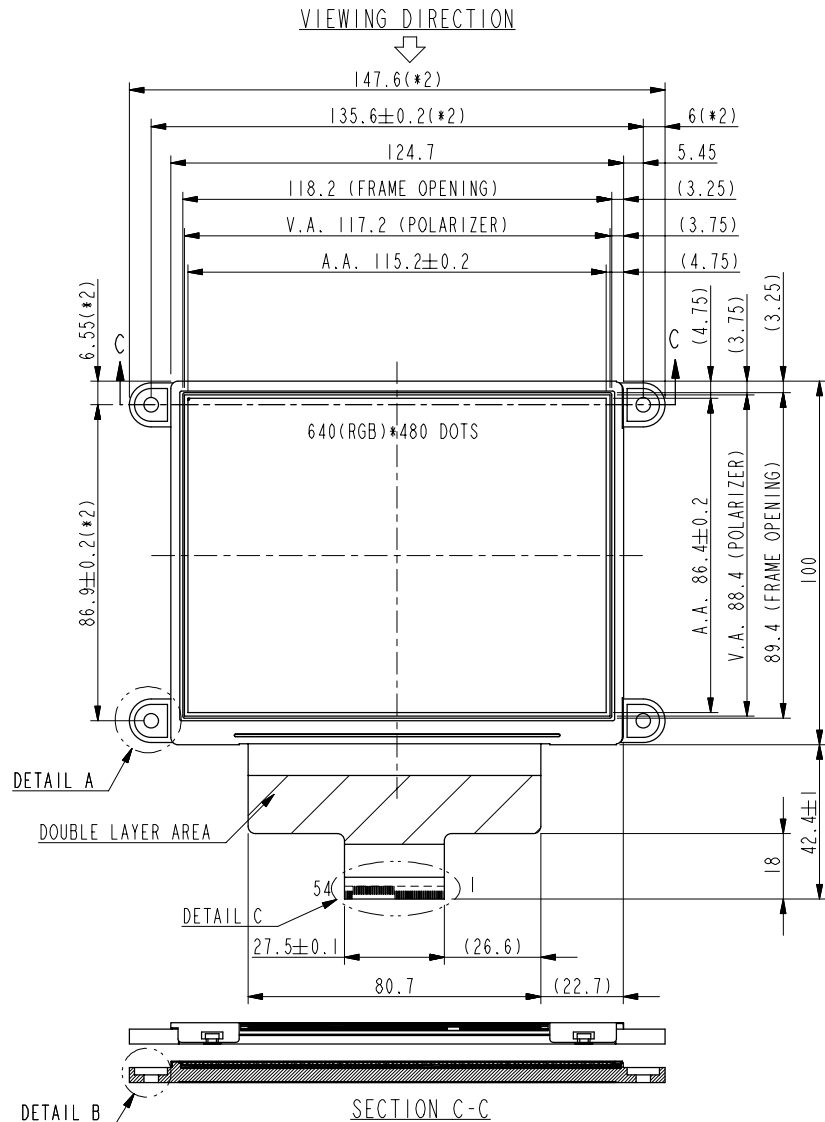


UNIT : mm

## 6.3 THE CALCULATING METHOD OF UNIFORMITY

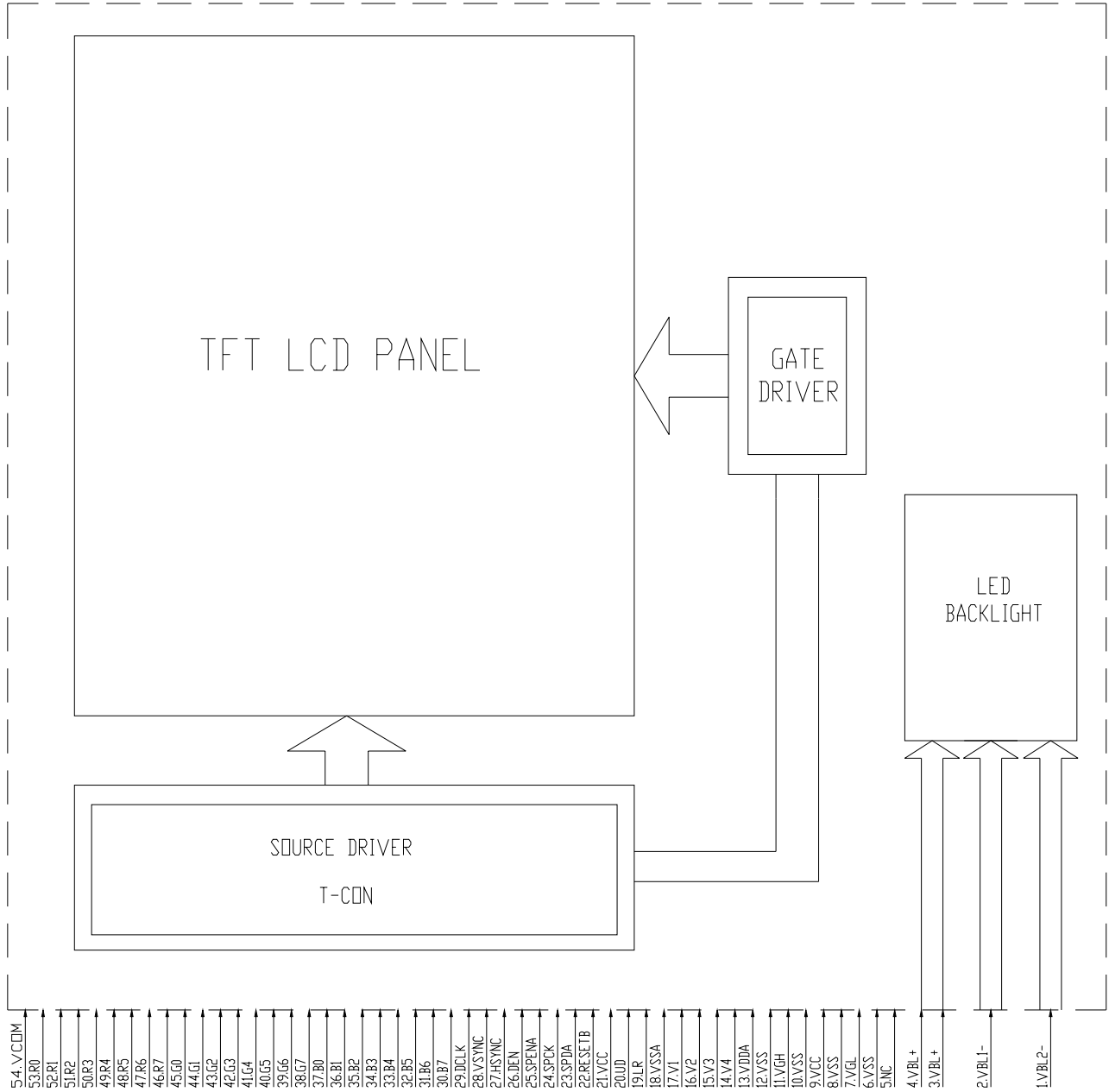
$$\text{UNIFORMITY} = \left[ 1 - \frac{\text{MAXIMUM BRIGHTNESS} - \text{MINIMUM BRIGHTNESS}}{\text{AVERAGE BRIGHTNESS}} \right] \times 100\%$$

7. OUTLINE DIMENSIONS

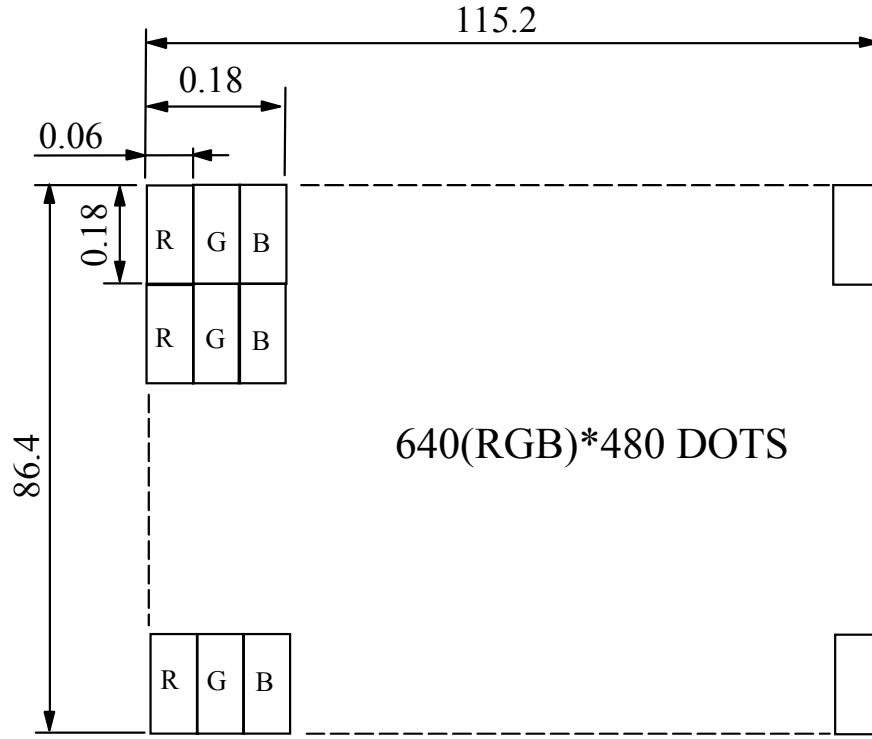


UNIT : mm  
SCALE : NTS  
NOT SPECIFIED TOLERANCE IS ± 0.5  
NOTE : MARK △ MODIFY (NUMBER NOTE MODIFY VERSION)  
NOTE:  
RECOMMEND MATCH CONNECTOR  
KYOCERA:04 6240 054 SERIES

8. BLOCK DIAGRAM



9. DETAIL DRAWING OF DOT MATRIX



UNIT : mm

SCALE : NTS

NOT SPECIFIED TOLERANCE IS  $\pm 0.1$

DOTS MATRIX TOLERANCE IS  $\pm 0.01$

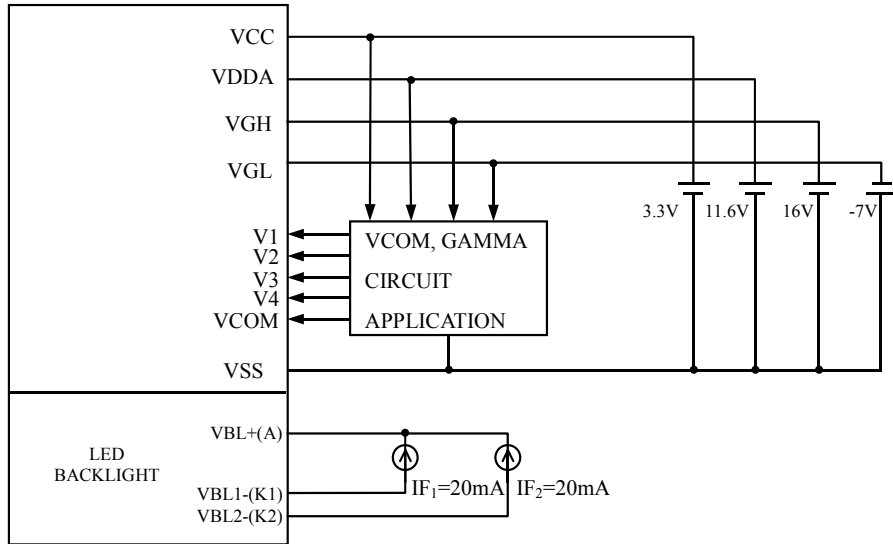
10. INTERFACE SIGNALS

PIN NO	SYMBOL	I/O	FUNCTION
1	VBL2-	P	BACKLIGHT LED GROUND (K2)
2	VBL1-	P	BACKLIGHT LED GROUND (K1)
3	VBL+	P	BACKLIGHT LED POWER (A)
4	VBL+	P	BACKLIGHT LED POWER (A)
5	NC	—	NOT USE
6	VSS	P	GROUND
7	VGL	P	GATE OFF POWER
8	VSS	P	GROUND
9	VCC	P	DIGITAL POWER
10	VSS	P	GROUND
11	VGH	P	GATE ON POWER
12	VSS	P	GROUND
13	VDDA	P	ANALOG POWER
14	V4	P	GAMMA CORRECTION VOLTAGE INPUT PINS
15	V3	P	GAMMA CORRECTION VOLTAGE INPUT PINS
16	V2	P	GAMMA CORRECTION VOLTAGE INPUT PINS
17	V1	P	GAMMA CORRECTION VOLTAGE INPUT PINS
18	VSSA	P	GROUND
19	LR	I	LR=H:STH→S01→ ---- → S0960 → STHO LR=L:STH→S960→ ---- → S01 → STHO
20	UD	I	UP/DOWN SCAN SETTING UD=H, REVERSE SCAN UD=L, NORMAL SCAN
21	VCC	P	DIGITAL POWER
22	RESETB	I	HARDWARE RESET
23	SPDA	I	SPI INTERFACE DATA. DEFAULT PULL HIGH.
24	SPCK	I	SPI INTERFACE DATA CLOCK. DEFAULT PULL HIGH.
25	SPENA	I	SPI INTERFACE DATA ENABLE SIGNAL. DEFAULT PULL HIGH.
26	DEN	I	DATA ENABLE INPUT
27	HSYNC	I	HORIZONTAL SYNC INPUT
28	VSYNC	I	VERTICAL SYNC INPUT
29	DCLK	I	DOT DATA COLOCK
30	B7	I	BLUE DATA BIT 7
31	B6	I	BLUE DATA BIT 6
32	B5	I	BLUE DATA BIT 5
33	B4	I	BLUE DATA BIT 4

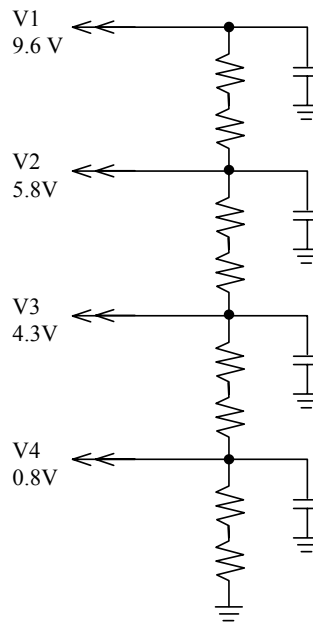
PIN NO	SYMBOL	I/O	FUNCTION
34	B3	I	BLUE DATA BIT 3
35	B2	I	BLUE DATA BIT 2
36	B1	I	BLUE DATA BIT 1
37	B0	I	BLUE DATA BIT 0
38	G7	I	GREEN DATA BIT 7
39	G6	I	GREEN DATA BIT 6
40	G5	I	GREEN DATA BIT 5
41	G4	I	GREEN DATA BIT 4
42	G3	I	GREEN DATA BIT 3
43	G2	I	GREEN DATA BIT 2
44	G1	I	GREEN DATA BIT 1
45	G0	I	GREEN DATA BIT 0
46	R7	I	RED DATA BIT 7
47	R6	I	RED DATA BIT 6
48	R5	I	RED DATA BIT 5
49	R4	I	RED DATA BIT 4
50	R3	I	RED DATA BIT 3
51	R2	I	RED DATA BIT 2
52	R1	I	RED DATA BIT 1
53	R0	I	RED DATA BIT 0
54	VCOM	P	DRIVING INPUT

1 1 . POWER SUPPLY

1 1 . 1 POWER SUPPLY FOR LCM

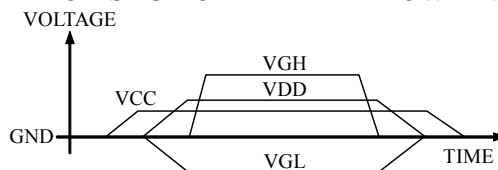


1 1 . 2 GAMMA CIRCUI



1 1 . 3 POWER SEQUENCE

THE LCD PANEL ADOPTS HIGH VOLTAGE DRIVER ICS, SO IT COULD BE PERMANENTLY DAMAGED IF A WRONG POWER ON/OFF SEQUENCE IS USED. WHEN POWERING ON THE LCD, VCC SHOULD GO UP FIRSTLY, AND THEN TURN ON VGL AND VDD, AND FINALLY VGH. TURN OFF THE LCD PANEL WITH REVERSED ORDER OR SHUT OFF ALL THE POWER SUPPLIES SIMULTANEOUSLY.



12 . INSPECTION CRITERION

12.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.( E.D.T ) TO CUSTOMERS

12.2 INSPECTION CONDITIONS

12.2.1 (1)OBSERVATION DISTANCE : 35cm±5cm

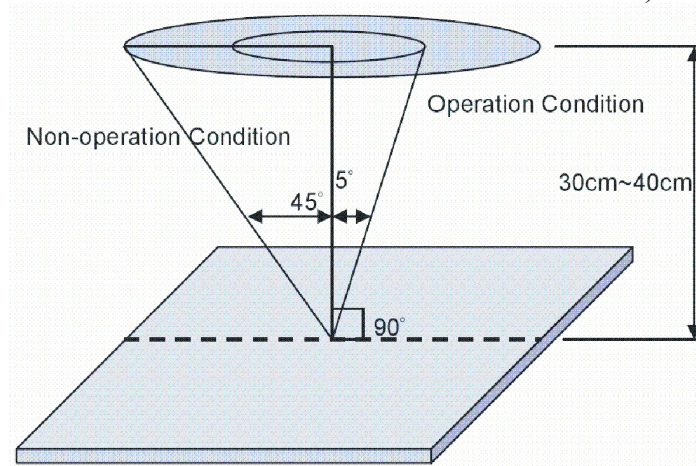
(2)VIEW ANGLE :

NON-OPERATION CONDITION : ±5°

(PERPENDICULAR TO LCD PANEL SURFACE)

OPERATION CONDITION : ±45°

(PERPENDICULAR TO LCD PANEL SURFACE)



12.2.2 ENVIRONMENT CONDITIONS :

AMBIENT TEMPERATURE		20°C~25°C
AMBIENT HUMIDITY		65±20%RH
AMBIENT ILLUMINATION	COSMETIC INSPECTION	MORE THAN 600Lux
	FUNCTIONAL INSPECTION	300~500 Lux

12.2.3 INSPECTION LOT

QUANTITY PER DELIVERY LOT FOR EACH MODEL

12.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY

(a)APPLICABLE STANDARD :

MIL-STD-105E

NORMAL INSPECTION, SINGLE SAMPLING

LEVEL II

(b)AQL : MAJOR DEFECT : AQL 0.65%

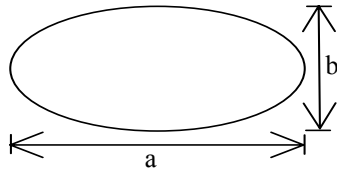
MINOR DEFECT : AQL 1.0%

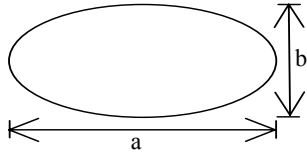
12.3 INSPECTION STANDARDS

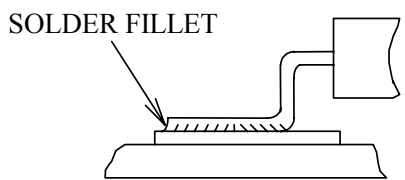
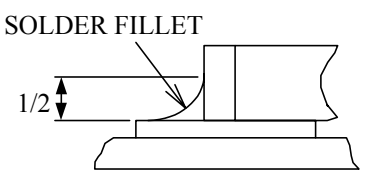
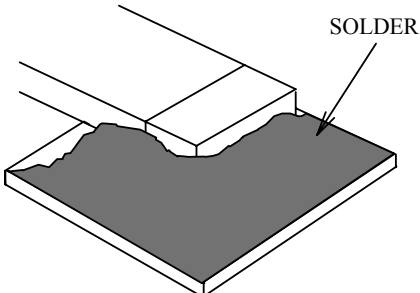
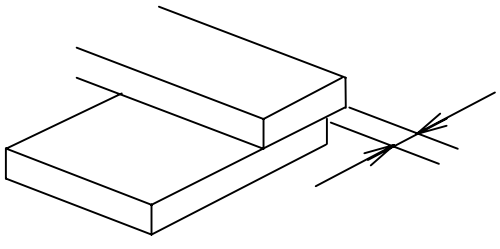
12.3.1 VISUAL DEFECTS CLASSIFICATION

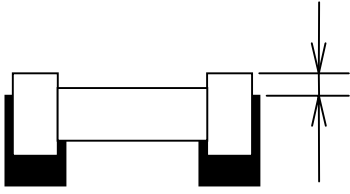
TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
MAJOR DEFECT	1.DISPLAY ON	<ul style="list-style-type: none"> <li>• DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS</li> <li>EX: DISCONNECTION, SHORT CIRCUIT ETC</li> </ul>	0.65
	2.BACKLIGHT	<ul style="list-style-type: none"> <li>• NO LIGHT</li> <li>• FLICKERING AND OTHER ABNORMAL ILLUMINATION</li> </ul>	
	3.DIMENSIONS	<ul style="list-style-type: none"> <li>• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS</li> </ul>	
MINOR DEFECT	1.DISPLAY ZONE	<ul style="list-style-type: none"> <li>• BLACK/WHITE SPOT</li> <li>• BUBBLES ON POLARIZER</li> <li>• NEWTON RING</li> <li>• BLACK/WHITE LINE</li> <li>• SCRATCH</li> <li>• CONTAMINATION</li> <li>• LEVER COLOR SPREED</li> </ul>	1.0
	2.BEZEL ZONE	<ul style="list-style-type: none"> <li>• STAINS</li> <li>• SCRATCHES</li> <li>• FOREIGN MATTER</li> </ul>	
	3.SOLDERING	<ul style="list-style-type: none"> <li>• INSUFFICIENT SOLDER</li> <li>• SOLDERED IN INCORRECT POSITION</li> <li>• CONVEX SOLDERING SPOT</li> <li>• SOLDER BALLS</li> <li>• SOLDER SCRAPS</li> </ul>	
	4.DISPLAY ON (ALL ON)	<ul style="list-style-type: none"> <li>• LIGHT LINE</li> </ul>	

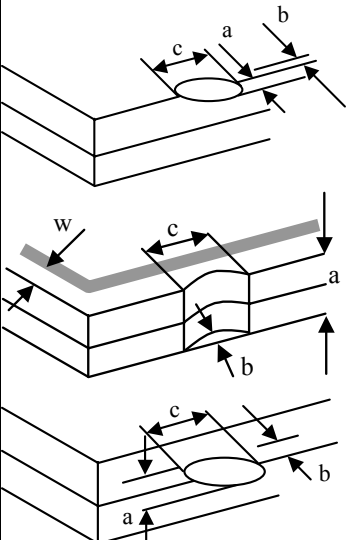
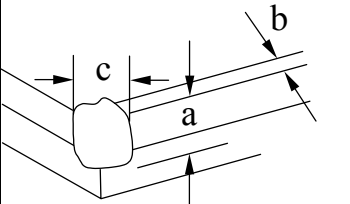
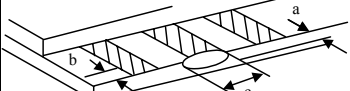
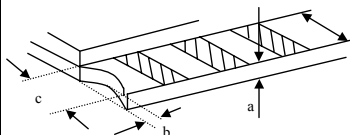
13.3.2 MODULE DEFECTS CALSSIFICATION

NO.	ITEM	CRITERIA												
1.	DISPLAY ON INSPECTION	(1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND SPEC												
2.	OVERALL DIMENSIONS	(1)OVERALL DIMENSION BEYOND SPEC												
3.	DOT DEFECT	(1) INSPECTION PATTERN: FULL WHITE, FULL BLACK, RED, GREEN AND BLUE SCREENS. (2) <table border="1" style="margin-left: 20px;"> <thead> <tr> <th>ITEMS</th> <th>ACCEPTABLE COUNT</th> </tr> </thead> <tbody> <tr> <td>BRIGHT DOT</td> <td><math>N \leq 2</math></td> </tr> <tr> <td>DARK DOT</td> <td><math>N \leq 3</math></td> </tr> <tr> <td>TOAL BRIGHT AND DARK DOTS</td> <td><math>N \leq 4</math></td> </tr> </tbody> </table> <p>NOTE :</p> <p>1. THE DEFINITION OF DOT : THE SIZE OF A DEFECTIVE DOT OVER 1/2 OF WHOLE DOT IS REGARDED AS ONE DEFECTIVE DOT.</p> <p>2. BRIGHT DOT : DOTS APPEAR BRIGHT AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER BLACK PATTERN.</p> <p>3. DARK DOT : DOTS APPEAR DARK AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER PURE RED, GREEN, BLUE PICTURE.</p>	ITEMS	ACCEPTABLE COUNT	BRIGHT DOT	$N \leq 2$	DARK DOT	$N \leq 3$	TOAL BRIGHT AND DARK DOTS	$N \leq 4$				
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DARK DOT	$N \leq 3$													
TOAL BRIGHT AND DARK DOTS	$N \leq 4$													
4.	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA	<table border="1" style="margin-left: 20px;"> <thead> <tr> <th>LENGTH : L</th> <th>WIDTH : W</th> <th>PERMISSIBLE NO.</th> </tr> </thead> <tbody> <tr> <td><math>L \leq 0.3</math></td> <td><math>W \leq 0.05</math></td> <td>IGNORE</td> </tr> <tr> <td><math>0.3 &lt; L \leq 2.5</math></td> <td><math>0.05 &lt; W \leq 0.1</math></td> <td>4</td> </tr> <tr> <td><math>2.5 &lt; L</math></td> <td><math>0.1 &lt; W</math></td> <td>NONE</td> </tr> </tbody> </table> <p>WIDTH : W mm, LENGH : L mm</p>	LENGTH : L	WIDTH : W	PERMISSIBLE NO.	$L \leq 0.3$	$W \leq 0.05$	IGNORE	$0.3 < L \leq 2.5$	$0.05 < W \leq 0.1$	4	$2.5 < L$	$0.1 < W$	NONE
LENGTH : L	WIDTH : W	PERMISSIBLE NO.												
$L \leq 0.3$	$W \leq 0.05$	IGNORE												
$0.3 < L \leq 2.5$	$0.05 < W \leq 0.1$	4												
$2.5 < L$	$0.1 < W$	NONE												
5.	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	<table border="1" style="margin-left: 20px;"> <thead> <tr> <th>AVERAGE DIAMETER (mm): D</th> <th>NUMBER OF PIECES PERMITTED</th> </tr> </thead> <tbody> <tr> <td><math>D \leq 0.15</math></td> <td>IGNORE</td> </tr> <tr> <td><math>0.15 &lt; D \leq 0.5</math></td> <td>4</td> </tr> <tr> <td><math>0.5 &lt; D</math></td> <td>NONE</td> </tr> </tbody> </table> <p>NOTE : DIAMETER <math>D=(a+b)/2</math></p> 	AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED	$D \leq 0.15$	IGNORE	$0.15 < D \leq 0.5$	4	$0.5 < D$	NONE				
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$D \leq 0.15$	IGNORE													
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$0.5 < D$	NONE													

NO.	ITEM	CRITERIA		
			AVERAGE DIAMETER (mm) : D	NUMBER OF PIECES PERMITTED
6.	BUBBLES OF POLARIZER /DIRT/CF FAIL /SURFACE STAINS	BUBBLE ON THE POLARIZER	$D \leq 0.25$	IGNORE
			$0.25 < D \leq 0.5$	$N \leq 5$
			$0.5 < D$	NOTE
		SURFACE STATUS	$D < 0.1 \text{ mm}$	IGNORE
			$0.1 < D \leq 0.3\text{mm}$	$N \leq 3$
		CF FAIL / SPOT	$D < 0.1 \text{ mm}$	IGNORE
$0.1 < D \leq 0.3\text{mm}$	$N \leq 3$			
		<p>NOTE : (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA.</p> <p>(2)THE EXTRANEIOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON.</p> <p>(3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING.</p> <p>AVERAGE DIAMETER (D)=(a+b)/2</p> 		
7.	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOW		
8.	MURA ON DISPLAY	IT'S OK IF MURA IS SLIGHT VISIBLE THROUG 6% ND FILTER		
9.	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.		
10.	BEZEL APPEARANCE	(1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.		
11	PCB	<p>(1)THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES.</p> <p>(2)NO OXIDATION OR CONTAMINATION PCB TERMINALS.</p> <p>(3)PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART. THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS.</p> <p>(4)THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART.</p> <p>(5)IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD, MAKE SURE IT IS SMOOTHED DOWN.</p>		

NO.	ITEM	CRITERIA
12.	SOLDERING	<p>(1)NO SOLDERING FOUND ON THE SPECIFIED PLACE</p> <p>(2)INSUFFICIENT SOLDER</p> <p>(a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD</p>  <p>(b)CHIP COMPONENT · SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING</p>  <ul style="list-style-type: none"> <li>• SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED</li> </ul>  <p>(3)PARTS ALIGMENT</p> <p>(a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE</p> 

NO.	ITEM	CRITERIA
12.	SOLDERING	<p>(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE</p>  <p>(4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB.            (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE.            (6)NO RESIDUE OR SOLDER BALLS ON PCB.            (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.</p>
13.	BACKLIGHT	<p>(1)NO LIGHT            (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION            (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS.            (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.</p>
14.	GENERAL APPEARANCE	<p>(1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP.            (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP.            (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT.            (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS.            (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER.            (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR.            (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED.            (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET.            (9)LCD PIN LOOSE OR MISSING PINS.            (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET.            (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET.            (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.</p>

NO.	ITEM	CRITERIA										
15.	CRACKED GLASS	<p>THE LCD WITH EXTENSIVE CRACK IS NOT ACCEPTABLE</p>										
		<p>GENERAL GLASS CHIP :</p> 	<table border="1" data-bbox="935 416 1458 499"> <thead> <tr> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td><math>\leq t/2</math></td> <td>&lt; VIEWING AREA</td> <td><math>\leq 1/8X</math></td> </tr> <tr> <td><math>t/2 &gt;, \leq 2t</math></td> <td><math>\leq W/2</math></td> <td><math>\leq 1/8X</math></td> </tr> </tbody> </table> <p>*W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE X = LCD SIDE LENGTH t = GLASS THICKNESS</p>	a	b	c	$\leq t/2$	< VIEWING AREA	$\leq 1/8X$	$t/2 >, \leq 2t$	$\leq W/2$	$\leq 1/8X$
		a	b	c								
		$\leq t/2$	< VIEWING AREA	$\leq 1/8X$								
		$t/2 >, \leq 2t$	$\leq W/2$	$\leq 1/8X$								
<p>CORNER PART :</p> 	<table border="1" data-bbox="935 1005 1458 1088"> <thead> <tr> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td><math>\leq t/2</math></td> <td>&lt; VIEWING AREA</td> <td><math>\leq 1/8X</math></td> </tr> <tr> <td><math>&gt; t/2, \leq 2t</math></td> <td><math>\leq W/2</math></td> <td><math>\leq 1/8X</math></td> </tr> </tbody> </table> <p>*W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE X = LCD SIDE LENGTH t = GLASS THICKNESS</p>	a	b	c	$\leq t/2$	< VIEWING AREA	$\leq 1/8X$	$> t/2, \leq 2t$	$\leq W/2$	$\leq 1/8X$		
a	b	c										
$\leq t/2$	< VIEWING AREA	$\leq 1/8X$										
$> t/2, \leq 2t$	$\leq W/2$	$\leq 1/8X$										
<p>CHIP ON ELECTRODE PAD</p> 	<table border="1" data-bbox="935 1281 1458 1341"> <thead> <tr> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td><math>\leq t</math></td> <td><math>\leq 0.5\text{mm}</math></td> <td><math>\leq 1/8X</math></td> </tr> </tbody> </table> <p>* X=LCD SIDE WIDTH t=GLASS THICKNESS</p>	a	b	c	$\leq t$	$\leq 0.5\text{mm}$	$\leq 1/8X$					
a	b	c										
$\leq t$	$\leq 0.5\text{mm}$	$\leq 1/8X$										
	<table border="1" data-bbox="935 1435 1458 1496"> <thead> <tr> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td><math>\leq t</math></td> <td><math>\leq 1/8X</math></td> <td><math>\leq L</math></td> </tr> </tbody> </table> <p>*X=LCD SIDE WIDTH t = GLASS THICKNESS L=ELECTRODE PAD LENGTH ①IF GLASS CHIPPING THE ITO TERMINAL, OVER 2/3 OF THE ITO MUST REMAIN AND BE, INSPECTED ACCORDING TO ELECTRODE TERMINAL SPECIFICATIONS ②IF THE PRODUCT WILL BE HEAT SEALED BY THE CUSTOMER, THE ALIGNMENT MARK MUST NOT BE DAMAGED</p>	a	b	c	$\leq t$	$\leq 1/8X$	$\leq L$					
a	b	c										
$\leq t$	$\leq 1/8X$	$\leq L$										

## 12.4 RELIABILITY TEST

### 12.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HIGH TEMP / HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: -40°C FOR 30 MINUTES ~ +85°C FOR 30 MINUTES
7	ESD (ELECTROSTATIC DISCHARGE) (NOT OPERATED)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV

NOTE (1) : THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

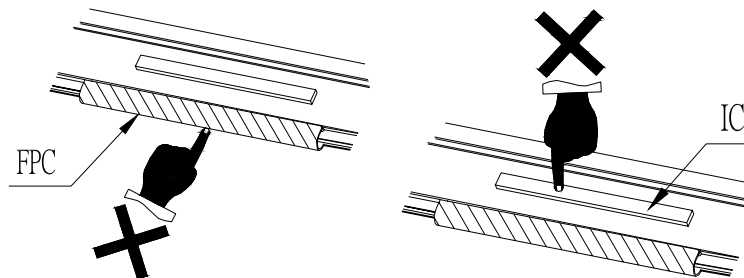
## 12.5 TESTING CONDITIONS AND INSPECTION CRITERIA

FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 12.5, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

## 12.6 OPERATION

- 12.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 12.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE ; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY ; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR . WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY .
- 12.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 12.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE .  
IF ABOVE SEQUENCE IS NOT FOLLOWED , CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH - UP PROBLEM .
- 12.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!  
DO NOT STRESS FPC AND IC ON THE MODULE!



## 12.7 NOTICE

- 12.7.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD .
- 12.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 12.7.3 DO NOT CHARGE STATIC ELECTRICITY , AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNAL SHOULD BE OF STATIC-PROTECTED MATERIAL .
- 12.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE ; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE .
- 12.7.5 DON'T GIVE EXTERNAL SHOCK.
- 12.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 12.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.  
WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 12.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 12.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 12.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 12.7.11 REWIRING: NO MORE THAN 3 TIMES.